



A parametric study on thermal performance of microchannel heat sinks with internally vertical bifurcations in laminar liquid flow



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ABSTRACT

The optimal position of internal vertical bifurcation integrated with a microchannel heat sink (MHS) is investigated numerically in the present research. The corresponding rectangular smooth microchannel is compared with those with internal vertical bifurcation. The optimal position along the streamwise direction for interval distances is studied in detail. The corresponding temperature fields, flow fields, pressure drop and thermal characteristics are presented through verified computational model. The numerical simulation indicates that a clear inflection point of pressure gradient may prevail with the presence of internal vertical bifurcation. It is also found that the microchannel heat sink with a small distance between the tail end of internal vertical bifurcation and the outlet of microchannel shows the best thermal performance instead of those with setting the tail end of internal bifurcation at the outlet of microchannel. The proposed optimal design of internal vertical bifurcation shows improved thermal performance without any pressure drop penalty.

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1. Introduction

With the advent of high-performance electronics, the problem of increasing heat dissipation may limit further development of ultra-dense circuitry. Yet more sophisticated and enormous packing density comes with shrinkage size of electronic devices as evolution goes further. Consequently, the generated tremendous amount of heat requires exquisite thermal management. Apparently the associated high-density assembly accompanies huge heat flux which may impose an obvious impact on the reliability of the electronic equipment. In general, reliability can be reduced by half for every 10 °C rise of junction temperature of electronics. Moreover, when temperature increases from 75 to 125 °C, reliability is reduced to 20% of the original value [1]. Therefore, it is imperative and a must to tailor the heat dissipation of high-density electronics first before they can reach the consumer market. Hence some typical efficient thermal management of electronics must be made and some typical designs can be depicted in [2–4].

For high flux thermal management, it appears that microchannel heat sinks (MCHS) first suggested by Tucker and Pease [5] in 1981 is among the best candidates to ease the gigantic heat problem. They employed purified water in microchannels, which is fab-

ricated in silicon chips and conducted a series of experiments with microchannel arrays. Their results of MCHS with water showed that the maximum temperature could be controlled within 71 °C subject to the highest flux of 790 W/cm². More researchers continued to enhance the MCHS capability for it offers advantages such as surface area to volume, simple structure, small mass and size, high convective heat transfer coefficient. Qu and Mudawer [6] studied the heat transfer characteristics in rectangular MCHS using water as the cooling fluid. In their research, the temperature increased linearly alongside the flow direction. Lee and Garimella [7] studied fluid flow process and heat transfer in MCHS and provided some equations to design microchannels heat sinks in detail. They continued to carry out a series of experimental investigations in rectangular microchannels with the width ranging from 194 to 534 μm [8]. They showed that conventional Navier-Stokes equation is still applicable to predict flow and heat transfer in MCHS. Rezanian et al. [9] experimentally investigated the feasibility of MCHS in thermoelectric applications. Peng and Peterson [10] experimentally found that the cross sectional aspect ratio casts significant influence on the pressure drop and heat transfer characteristics of water flowing in smooth rectangular MCHS.

Analytical methods had been made to investigate MCHS. Wang et al. [11,12] conducted analyses to optimize the geometric structures of rectangular MCHS subject to constraint conditions. Shafeie et al. [13], Esmailnejad et al. [14] and Mitalare [15] presented

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