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Soldering of solution-processed organic vertical transistor 3 and light-emitting diode on separate glass substrates by tin micro-balls ☆

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ABSTRACT

The vertical organic space-charge-limited transistor made of P3HT and small-molecule 31 32 phosphorescent organic light-emitting diode (OLED) are made on two separate glass substrate by blade coating, then soldered vertically together by tin balls with 40 µm diameter. 33 The soldering is done by hot wind of 150 °C for 5 min Contact resistance is only 10 Ω . The 34 vertical transistor is annealed at 150 °C for 5 min before soldering to enhance the output 35 current up to 25 mA/cm² and give high thermal stability. Both OLED and the annealed ver-36 tical transistor are not affected by the soldering process. The vertical transistor has 1/4 of 37 38 the OLED area and turns on the bottom-emission white OLED up to 300 cd/m^2 and orange 39 OLED up to 600 cd/m². The entire operation is within 8 V. OLED and transistor array can 40 therefore be made on separate glass substrates then soldered together to form the display. © 2013 The Authors. Published by Elsevier B.V. All rights reserved.

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1. Introduction 44

45 Active-matrix organic light-emitting diode (AMOLED) is an emerging technology for display. It has the advantages 46 of good color quality, high response speed, and low thick-47 ness compared with the conventional liquid crystal dis-48 49 play. A variety of vacuum-processed transistor backplanes are used for the pixel driving circuit of 50 AMOLED including amorphous silicon, poly-crystalline sil-51 52 icon, oxide semiconductor like IGZO [1-3], and organic 53 field-effect transistors [4,5]. The OLED and the transistor 54 are usually fabricated on the same glass substrates

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[1-5]. The aperture ratio is limited by the pixel area 55 occupied by the transistors, therefore the OLED can only 56 have top emission [3]. In top-emission OLED the device 57 structure is usually more complicated and the view angle 58 is narrow because of the optical cavity effect. Furthermore, 59 the efficiency of white OLED is greatly reduced in the top-60 emission structure relative to the conventional bottom 61 emission structure. This is because only part of the broad 62 white emission spectrum matches the cavity resonance 63 wavelength. For large area AMOLED white OLED in combi-64 nation with color filter has the great advantage that no pat-65 terning is necessary for all the organic semiconductor 66 layers. Indeed the difficulty related to the shadow mask 67 patterning for individual red, green, and blue OLED in-68 creases dramatically with display size. If the OLED and 69 the transistor can be fabricated on separate substrates 70 and connected together afterwards, OLED with convenient 71 bottom emission structure and white OLED without any 72 patterning can be used in AMOLED. In addition, such 73

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afterward connection avoids the potential conflict in OLED
and transistor process temperature and the devices can be
individually optimized. Connection of OLED and amorphous-silicon on separate substrates are reported by direct
hard metal contact [6]. Without any soldering such contact
gives high contact resistance [6], and poor uniformity is

expected due to the inevitable random air gap up to $10 \,\mu\text{m}$ between the glass substrates resulting from the glass unevenness. Large-area AMOLED will become practical only if both OLED and the transistor can be solution-processed without patterning, and they can be reliably soldered together after separated optimized fabrication.



Fig. 1. (a) Schematic diagram of OLED and SCLT after soldering by tin balls. The device structure consists of two separate glass substrate, i.e., the bottom substrate involving SCLT with SAM treatment and the top substrate involving bottom-emission white OLED. (b) The SEM cross-section image of SCLT structure. (c) The active areas and relative position of the two devices.

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