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## Review Article



# Development of high-thermal-conductivity silicon nitride ceramics

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#### ABSTRACT

Silicon nitride  $(Si_3N_4)$  with high thermal conductivity has emerged as one of the most promising substrate materials for the next-generation power devices. This paper gives an overview on recent developments in preparing high-thermal-conductivity  $Si_3N_4$  by a sintering of reaction-bonded silicon nitride (SRBSN) method. Due to the reduction of lattice oxygen content, the SRBSN ceramics could attain substantially higher thermal conductivities than the  $Si_3N_4$  ceramics prepared by the conventional gas-pressure sintering of silicon nitride (SSN) method. Thermal conductivity could further be improved through increasing the  $\beta/\alpha$  phase ratio during nitridation and enhancing grain growth during post-sintering. Studies on fracture resistance behaviors of the SRBSN ceramics revealed that they possessed high fracture toughness and exhibited obvious *R*-curve behaviors. Using the SRBSN method, a  $Si_3N_4$  with a record-high thermal conductivity elements, Fe and Al, on thermal conductivities of the SRBSN ceramics revealed that the tolerable content limits for the two impurities were different. While 1 wt% of impurity Fe hardly degraded thermal conductivity, only 0.01 wt% of Al caused large decrease in thermal conductivity.

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#### 1. Introduction

Energy and environment-related problems are serious social issues. In order to save energy as well as to reduce the emission of carbon dioxide, energy sources tend to shift from fossil

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fuel to electric power, hence highly efficient use of electric power becomes more and more important. Power electronic devices are key technologies for this purpose, and they have been widely used for a variety of applications such as industrial robots, hybrid motor vehicles, and advanced electric trains [1]. Driven by the demand for more efficient control and conversion of electric power, power device technology is advancing toward higher voltage, larger current, greater power density, and this trend is poised to be accelerated with the replacement of Si by the wide-band gap semiconductors (SiC and GaN) in the near future [2,3]. However, the high power will induce large thermal stresses in the devices, which poses great challenges for the assembly of the devices and the

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packaging materials, especially the brittle ceramic substrates that provide functions of electrical insulation and heat dissipation. Therefore, both good mechanical reliability and high thermal conductivity are required for the ceramic substrates used for the high-power electronic devices.

So far, AlN has been used as a major ceramic substrate material for power devices because it exhibits high thermal conductivity over 200 Wm<sup>-1</sup> K<sup>-1</sup>. However, mechanical properties of AlN are not sufficient (generally, bending strength of 300–400 MPa, fracture toughness of 3–4 MPa m<sup>1/2</sup>), which results in low reliability of the substrates. The electronic industry is eager to seek alternative high-thermal-conductivity substrate materials with good mechanical properties, therefore attention is turned to Si<sub>3</sub>N<sub>4</sub> ceramics.

Over the last decades, Si<sub>3</sub>N<sub>4</sub> ceramics have been investigated as high temperature structural materials, and now Si<sub>3</sub>N<sub>4</sub> with bending strength over 1 GPa can be prepared by choosing appropriate sintering additives and fully developing a bimodal microstructure composed of interlocked rod-like grains [4]. However, thermal conductivity of  $Si_3N_4$  ceramics is usually rather low (<70 Wm<sup>-1</sup> K<sup>-1</sup>). If thermal conductivity of Si<sub>3</sub>N<sub>4</sub> can be improved and at the mean time its good mechanical properties can be maintained, Si<sub>3</sub>N<sub>4</sub> would undoubtedly be an attractive substrate material for highpower electronic device applications. Such an expectation has fascinated many researchers to improve thermal conductivities of Si<sub>3</sub>N<sub>4</sub> ceramics by a variety of means in recent years. In this paper, previous studies on thermal conductivity property of Si<sub>3</sub>N<sub>4</sub> are briefly reviewed. Following that, some recent achievements of our research group on fabricating high-thermal-conductivity Si<sub>3</sub>N<sub>4</sub> ceramics via a reaction-bonding and post-sintering method are introduced.

#### 2. Thermal conductivity of silicon nitride

 $Si_3N_4$  mainly exists in two hexagonal polymorphs, namely  $\alpha$ and β-Si<sub>3</sub>N<sub>4</sub>, which are generally regarded as low and high temperature crystal forms, respectively [5]. Industrial synthesis routes mainly lead to  $\alpha$ -Si<sub>3</sub>N<sub>4</sub>, which converts to the more stable  $\beta$ -Si<sub>3</sub>N<sub>4</sub> phase during high temperature sintering. Si<sub>3</sub>N<sub>4</sub> is a highly covalent compound, and it transports heat primarily by phonons at room temperature and below. In 1995, Haggerty and Lightfoot [6] predicted that the intrinsic thermal conductivity of Si<sub>3</sub>N<sub>4</sub> might be 200–320 Wm<sup>-1</sup> K<sup>-1</sup> at room temperature. Later, Watari et al. [7] estimated that the upper limit of the intrinsic thermal conductivity of  $\beta$ -Si<sub>3</sub>N<sub>4</sub> could be 400 Wm<sup>-1</sup> K<sup>-1</sup>. Moreover, Hirosaki et al. [8] estimated the theoretical thermal conductivity of single crystal  $\alpha$ - and  $\beta$ -Si<sub>3</sub>N<sub>4</sub>, using the molecular dynamics method in conjunction with the Green-Kubo formulation, as a function of temperature. In their calculation, the estimated thermal conductivities of  $\alpha$ - and  $\beta$ -Si<sub>3</sub>N<sub>4</sub>, along the *a*-axis and *c*-axis at room temperature were approximately 105 and 225 Wm<sup>-1</sup> K<sup>-1</sup>, and 170 and 450 Wm<sup>-1</sup> K<sup>-1</sup>, respectively. However, thermal conductivities of polycrystalline Si<sub>3</sub>N<sub>4</sub> ceramics are much lower than the intrinsic values of the single crystal due to the following reasons.

Owing to its strong covalency and low diffusivity, sinterability of  $Si_3N_4$  is poor.  $Si_3N_4$  is generally densified by liquid-phase-sintering mechanisms, where some oxide sintering aids are added and they react with  $Si_3N_4$  as well as the silica phase on the surface of  $Si_3N_4$  particles to form a liquid phase which promotes densification through rearrangement and solution-reprecipitation mechanisms during sintering. After sintering, the liquid phase converts to glassy or partially crystallized phases (oxynitrides) in the sintered material. They may exist as isolated secondary phases at the triple point junctions surrounded by three grains, or as a continuous thin film (around 1 nm thickness) on the boundaries between two adjacent grains. Because thermal conductivities of these oxynitride

secondary phases are quite low (less than  $5 \text{ Wm}^{-1} \text{ K}^{-1}$ ), their existence in the microstructure causes reduction of the thermal conductivity of the sintered material. Compared with the triple-junction phases, the detrimental effect of the grain boundary phases is greater due to its continuity. Kitayama et al. [9] assessed the effects of the grain boundary phases on the overall thermal conductivities of Si<sub>3</sub>N<sub>4</sub> ceramics by using a modified Wieners' model, and their calculation indicated that the detrimental effect of the grain sizes of the β-Si<sub>3</sub>N<sub>4</sub> grains were larger than several micrometers. Thus, promoting grain growth is an effective way of improving thermal conductivity. However, it should be aware that microstructural coarsening often results in lower mechanical strength of the material.

Besides the secondary and grain boundary phases which reside outside  $Si_3N_4$  grains, there exist a variety of imperfections called lattice defects (impurity atoms, vacancies, dislocations, stacking faults, etc.) within the  $Si_3N_4$  grains. Because  $Si_3N_4$  transports heat primarily by phonon (lattice vibration), lattice defects in  $Si_3N_4$ crystals can induce phonon scattering, thereby reducing thermal conductivity. It has been reported that solution of oxygen into  $Si_3N_4$ crystals generates vacancies at the Si sites in  $Si_3N_4$  lattice [10,11]. Si vacancies can scatter phonons and lead to lower thermal conductivity. In order to improve thermal conductivity, it is essential to lower the content of oxygen dissolved in  $Si_3N_4$  lattice (i.e., lattice oxygen content).

Therefore, choosing a Si<sub>3</sub>N<sub>4</sub> powder with low impurity oxygen content as a starting material is decisively important for preparing Si<sub>3</sub>N<sub>4</sub> ceramics with high thermal conductivity. Sintering additives, which are indispensible for achieving densification of Si<sub>3</sub>N<sub>4</sub>, also play an important role in reducing lattice oxygen content of the sintered Si<sub>3</sub>N<sub>4</sub>. Using sintering additives with high oxygen affinity (e.g., rare earth oxides) [7,12], or choosing sintering additive compositions which lead to high nitrogen/oxygen ratios of the liquid phase during sintering were effective ways of reducing lattice oxygen content of the sintered Si<sub>3</sub>N<sub>4</sub>. For example, Hayashi et al. [13] reported that while a Si<sub>3</sub>N<sub>4</sub> ceramic with a thermal conductivity of 120 Wm<sup>-1</sup> K<sup>-1</sup> was prepared by using a Yb<sub>2</sub>O<sub>3</sub>-MgO sintering additive, the thermal conductivity could be increased to 140 Wm<sup>-1</sup> K<sup>-1</sup> by replacing MgO with MgSiN<sub>2</sub> in the sintering additive so that nitrogen/oxygen ratio of the liquid phase was increased.

However, for Si<sub>3</sub>N<sub>4</sub> starting powders, even the top grade highpurity commercial powder contains more than 1 wt% of oxygen residing both on the surface and within the lattice, which imposes an upper limit on the attainable thermal conductivity of Si<sub>3</sub>N<sub>4</sub> ceramics. In order to raise the upper limit to a higher level, a starting powder containing less oxygen should be used. Motivated by such an idea, our research group proposed a strategy of preparing high thermal conductivity Si<sub>3</sub>N<sub>4</sub> via a route of sintering of reactionbonded silicon nitride (SRBSN), which is a well-known process of fabricating Si<sub>3</sub>N<sub>4</sub> ceramics from an Si starting powder instead of a Si<sub>3</sub>N<sub>4</sub> powder [14–17], in consideration of the fact that Si powders containing much less oxygen and metallic impurities than Si<sub>3</sub>N<sub>4</sub> powders are commercially available thanks to the advancement of modern semiconductor industry. Our experimental results have verified the effectiveness of the SRBSN strategy, by which both higher thermal conductivity and higher mechanical strength were achieved, compared to the conventional sintering of silicon nitride powder (SSN) route [18–28].

### 3. High-thermal-conductivity silicon nitride via the SRBSN route

The sintering of reaction-bonded silicon nitride (SRBSN) method consists of two processing steps. Firstly, an Si compact composed of Si powder and sintering additives is heated to a temperature Download English Version:

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