

Thickness-dependent microstructures and electrical properties of $\text{CaCu}_3\text{Ti}_4\text{O}_{12}$ films derived from sol–gel process

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Abstract

$\text{CaCu}_3\text{Ti}_4\text{O}_{12}$ (CCTO) thin films with various thicknesses were prepared by a sol–gel multiple coating processes on Pt/Ti/SiO₂/Si substrates. Microstructures and surface morphologies of CCTO thin films were analyzed by grazing incident X-ray diffractometer (GIXRD) and scanning electron microscope (SEM), respectively. The correlation between the thickness and electrical properties of CCTO films was investigated. The dielectric constants of CCTO films decreased with increasing film thickness (coating cycle). Both the dielectric constant of CCTO films and interlayer are calculated. Possible mechanisms are explored to explain the thickness dependence of the dielectric constant of CCTO films.

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1. Introduction

Recently, the new perovskite-type material $\text{CaCu}_3\text{Ti}_4\text{O}_{12}$ (CCTO) has attracted much attention because of its high dielectric constant over a broad temperature range. Each successive dynamic random access memories (DRAM) generation has to maintain the same storage charge, while the area of the capacitor has significantly decreased [1–6]. CCTO is a promising candidate for application of DRAM in very large scale integrated (VLSI) circuits due to its large dielectric constant. With the shrinking of device dimensions, high dielectric constant materials become more important and necessary to be used for capacitor in DRAM. Increasing the dielectric constant of the material or the area of a capacitor can achieve a large capacitance.

The high dielectric constant is attributed to the grain-boundary capacitance (internal barrier layer capacitance, IBLC) instead of an intrinsic property associated with the crystal structure [7]. In order to apply CCTO in microelectronic devices and give a more fundamental understanding of its physical property, some groups have grown CCTO films by

pulsed-laser deposition (PLD) with different substrates [8,9]. In the work of Jiang et al., high quality epitaxial CCTO films were prepared on a (001)-oriented LaAlO₃ substrate and presented a high dielectric constant of 10⁴ at 1 MHz at room temperature [8]. While in the studies of Fang and Shen, the CCTO thin film deposited on Pt/Ti/SiO₂/Si substrates showed a polycrystalline characteristic and achieved a dielectric constant of near 2000 at 10 kHz and room temperature [9,10]. The process window of the above methods of CCTO thin film is very narrow. In addition, the PLD method for CCTO is expensive. Sol–gel was chosen as the preparation technique in this study because it offers a homogeneous distribution of elements on a molecular level, ease of composition control, high purity, and the ability to coat large and complex area substrate. Both the dielectric constants of CCTO films and interlayer are calculated by Mixture Rule. A pore fraction of dielectric film is also estimated.

2. Experimental procedures

The CCTO thin films were prepared by a sol–gel technique. Four inch diameter p-type (100) Si wafers with nominal resistivity of 5 to 10 Ω cm were used as substrates. After standard RCA cleaning, a 200 nm SiO₂ film was grown on Si substrate, then 10 nm Ti and 100 nm Pt layers were deposited sequentially by dc

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