



Current–voltage temperature characteristics of Au/n-Ge (1 0 0) Schottky diodes

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ABSTRACT

The variation in electrical characteristics of Au/n-Ge (1 0 0) Schottky contacts have been systematically investigated as a function of temperature using current–voltage (I – V) measurements in the temperature range 140–300 K. The I – V characteristics of the diodes indicate very strong temperature dependence. While the ideality factor n decreases, the zero-bias Schottky barrier height (SBH) (Φ_B) increases with the increasing temperature. The I – V characteristics are analyzed using the thermionic emission (TE) model and the assumption of a Gaussian distribution of the barrier heights due to barrier inhomogeneities at the metal–semiconductor interface. The zero-bias barrier height Φ_B vs. $1/2 kT$ plot has been used to show the evidence of a Gaussian distribution of barrier heights and values of $\Phi_B = 0.615$ eV and standard deviation $\sigma_{s0} = 0.0858$ eV for the mean barrier height and zero-bias standard deviation have been obtained from this plot, respectively. The Richardson constant and the mean barrier height from the modified Richardson plot were obtained as $1.37 \text{ A cm}^{-2} \text{ K}^{-2}$ and 0.639 eV, respectively. This Richardson constant is much smaller than the reported of $50 \text{ A cm}^{-2} \text{ K}^{-2}$. This may be due to greater inhomogeneities at the interface.

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1. Introduction

Metal–semiconductor (MS) contacts are the most widely used rectifying contacts in the electronic industry. The performance and reliability of a Schottky diode (SD) is drastically influenced by the interface quality between the deposited metal and semiconductor surface [1]. Therefore, MS interfaces are an essential part of virtually all semiconductor electronic and optoelectronic devices. The Schottky barrier height (SBH) is one of the most interesting properties of a MS interface as it controls the electron transport across the MS interface, and is of vital importance to the successful operation of any semiconductor device [2]. It is often found that the current–voltage (I – V) characteristics of MS contacts usually deviate from the ideal thermionic emission (TE) current model [3]. Analysis of the I – V characteristics of Schottky barrier diodes (SBDs) based on TE theory usually reveals an abnormal decrease in the barrier height and an increase in the ideality factor with decreasing temperature [4,5]. The standard TE theory fails to explain this result as it expects the SBH variation to

be controlled only by the variation of band gap with temperature [6]. SBH in Schottky contacts is likely to be a function of the atomic structure, and the atomic inhomogeneities at MS interface, which are caused by grain boundaries, defects, multiple phases, etc. [7]. Contaminants due to undesirable reaction products at the MS interface may act directly to introduce inhomogeneities or they may simply promote inhomogeneity through the generation of defects [8]. Schottky diodes with low SBH have found applications in devices operating at cryogenic temperatures as infrared detectors and sensors in thermal imaging [9–12]. Therefore, analysis of I – V characteristics of Schottky diodes at room temperature only does not give detailed information about their conduction process or the nature of barrier formation at the MS interface. The temperature dependence of the I – V characteristics allows us to understand different aspects of conduction mechanisms [13].

To the best of our knowledge there has been no report on the electrical transport characteristics of germanium (Ge) Schottky contacts at low temperatures. Therefore an attempt has been made to study the current transport characteristics of Au/n-Ge (1 0 0) Schottky diodes in the temperature range 140–300 K. In this study, the I – V characteristics of Au/n-Ge (1 0 0) Schottky diodes were measured in the 140–300 K temperature range. The temperature dependent barrier characteristics of Au/n-Ge (1 0 0)

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contact were interpreted on the basis of a thermionic emission mechanism with Gaussian spatial distribution of the barrier heights around a mean value due to barrier height inhomogeneities prevailing at the MS interface.

2. Experimental procedures

Schottky barrier diodes were fabricated on Sb-doped (1 0 0) n-type Ge substrate with doping concentration of $2.5 \times 10^{15} \text{ cm}^{-3}$. The substrate was sequentially degreased with organic solvents like trichloroethylene, acetone and methanol by ultrasonic agitation for 5 min in each stage followed by rinsing in deionized water. The native oxide on the surface was etched in a mixture of H_2O_2 (30%): H_2O (1:5) for 1 min. After rinsing in deionized water the sample was blown dry using N_2 . Immediately after cleaning the sample was inserted into a vacuum chamber where 100 nm thick AuSb (0.6%) was deposited by resistive evaporation as a back ohmic contact, followed by annealing at 350°C in Ar ambient for 10 min to minimize the ohmic contact's resistivity. Before contacts deposition, the sample was again chemically cleaned as described above. Au Schottky contacts were deposited by vacuum resistive evaporation through a mechanical mask. These contacts were deposited under vacuum with a pressure below 10^{-6} Torr. The contacts were (0.60 ± 0.05) mm in diameter and 30 nm thick. Thickness of the contacts was measured during deposition by an INFICON XTC 751-001-G1 crystal thickness monitor. After contact fabrication, current–voltage (I – V) measurements were performed in a He cryostat in the temperature range 140–300 K.

3. Results and discussions

3.1. The current–voltage characteristics as a function of temperature

The I – V relation for a MS diode, based on the TE theory can be expressed as [1,14]

$$I = I_s \exp\left(\frac{qV}{nkT}\right) \left[1 - \exp\left(-\frac{qV}{kT}\right)\right] \quad (1)$$

where I is the measured current, V is the applied voltage, q is the electronic charge, n is the ideality factor that describes the deviation from the ideal diode equation for reverse bias as well as forward bias, k is the Boltzmann's constant, T is the absolute temperature in Kelvin, I_s is the saturation current derived from the straight line intercept of $\ln I$ at zero-bias and is given by

$$I_s = AA^*T^2 \exp\left(-\frac{q\Phi_B}{kT}\right) \quad (2)$$

where A is the SD area, A^* is the effective Richardson constant and Φ_B is the apparent barrier height at zero-bias, which can be obtained from Eq. (2)

$$\Phi_B = \frac{kT}{q} \ln\left(\frac{AA^*T^2}{I_s}\right) \quad (3)$$

The ideality factor is determined from the slope of the linear region of the plot of natural log of forward current vs. forward bias voltage and is given by

$$n = \frac{q}{kT} \left(\frac{dV}{d(\ln I)}\right) \quad (4)$$

Typical forward bias I – V characteristics of Au/n-Ge (1 0 0) Schottky contacts measured in the 140–300 K temperature range are shown in Fig. 1. These curves indicate a very strong temperature dependence of the Schottky diodes. The I – V characteristics deviate from ideality at low temperature (140–200 K), which is

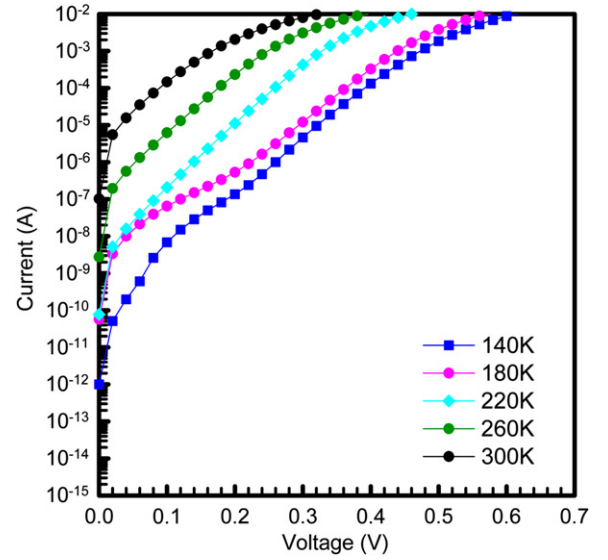


Fig. 1. Experimental forward-bias current–voltage characteristics of a Au/n-Ge (1 0 0) Schottky contact in the temperature range 140–300 K.

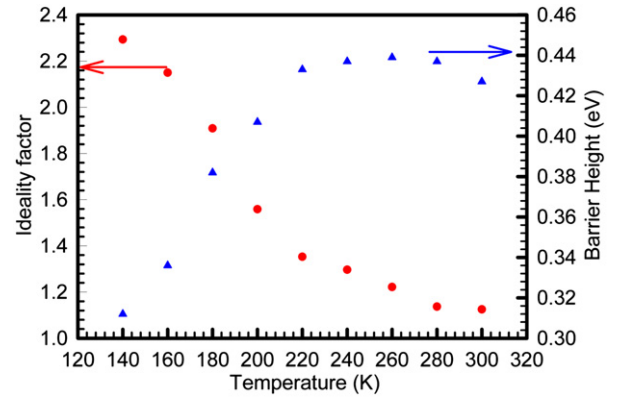


Fig. 2. Temperature dependence of ideality factor and barrier height for Au/n-Ge (1 0 0) Schottky contact in the temperature range 140–300 K.

due to the effect of other current transport processes like generation–recombination of carriers in space charge region [14] and tunneling of electrons through the barrier. At temperatures above 220 K, thermionic emission becomes the dominant process. We have performed the least squares fit of Eq. (1) to the linear part of the intermediate forward bias region of the measured I – V plots (Fig. 1), by assuming that this region consisted of TE current [1]. From these fits, the experimental values of Φ_B and n are determined.

Φ_B and n plots as a function of temperature for the Au/n-Ge (1 0 0) SBDs are presented in Fig. 2. The SBH shows a decrease with the decrease in temperature, while the ideality factor increase with decrease in temperature. This depicts that both parameters exhibit strong temperature dependence. This temperature dependence can be attributed to the discontinuities at the interface, which may exist even for well controlled fabrication of samples [15]. Since current flow across a MS interface is a temperature activated process [9,10,13,16], the current transport will be dominated by current flowing through nanometer scale interfacial patches of small regions with lower SBH and a larger ideality factor [13]. As a result of inhomogeneities, charge transport across the interface is no longer dominated by TE. Furthermore, many models have been devolved to explain the inhomogeneity in the barrier [9,17,18]. A potential fluctuation

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