



A comment on “An efficient common-multiplicand-multiplication method to the Montgomery algorithm for speeding up exponentiation”

Da-Zhi Sun^{a,b,*}, Jin-Peng Huai^b, Zhen-Fu Cao^c

^a School of Computer Science and Technology, Tianjin University, No. 92 Weijin Road, Nankai District, Tianjin 300072, PR China

^b School of Computer Science, Beihang University, Beijing 100083, PR China

^c Department of Computer Science and Engineering, Shanghai Jiao Tong University, 800 Dongchuan Road, Shanghai 200240, PR China

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ABSTRACT

In 2009, Wu proposed a fast modular exponentiation algorithm and claimed that the proposed algorithm on average saved about 38.9% and 26.68% of single-precision multiplications as compared to Dussé–Kaliski's Montgomery algorithm and Ha–Moon's Montgomery algorithm, respectively. However, in this comment, we demonstrate that Wu's algorithm on average reduces the number of single-precision multiplications by at most 22.43% and 6.91%, when respectively compared with Dussé–Kaliski's version and Ha–Moon's version. That is, the computational efficiency of Wu's algorithm is obviously overestimated.

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1. Introduction

The modular exponentiation is the dominant part of the implementation costs in many prevailing public-key cryptosystems. Therefore, Wu [6] proposed a fast modular exponentiation algorithm, of which the idea is to combine the common-multiplicand-multiplication (CMM) Montgomery method [4], the folding exponent method [3,5], and the minimal-signed-digit (MSD) recoding method [1]. According to Wu's claim, the proposed algorithm on average saved about 38.9% and 26.68% of single-precision multiplications as compared to Dussé–Kaliski's Montgomery algorithm [2] and Ha–Moon's Montgomery algorithm [4], respectively.

However, we demonstrate that Wu's algorithm on average reduces the number of single-precision multiplications by at most 22.43% and 6.91%, when respectively compared with Dussé–Kaliski's version and Ha–Moon's version. Our computational efficiency result is accurate, because all crucial operations in Wu's algorithm are considered exactly.

2. Brief description of Wu's method

For a self-contained discussion, we briefly review Wu's algorithm and refer the readers to [6] for more details about it. To compute the modular exponentiation $M^E \pmod{N}$, Wu's algorithm can be restated as follows:

Step 1. Divide the MSD representation $(e_{k-1} \cdots e_1 e_0)_{\text{MSD}}$ for the exponent E into three equal-length bit strings E_1 , E_2 , and E_3 , i.e. $E = E_1 \| E_2 \| E_3$, where $\|$ denotes the bit string concatenation.

* Corresponding author at: School of Computer Science and Technology, Tianjin University, No. 92 Weijin Road, Nankai District, Tianjin 300072, PR China. Tel./fax: +86 22 27406538.

E-mail addresses: sundazhi@tju.edu.cn, sundazhi1977@126.com (D.-Z. Sun).

Step 2. Compute

$$E_{com} = E_1 \text{ AND } E_2 \text{ AND } E_3 = (e_m^0 \cdots e_1^0 e_0^0), \quad (1)$$

$$E'_1 = E_1 \text{ XOR } E_{com} = (e_m^1 \cdots e_1^1 e_0^1), \quad (2)$$

$$E'_2 = E_2 \text{ XOR } E_{com} = (e_m^2 \cdots e_1^2 e_0^2), \quad (3)$$

$$E'_3 = E_3 \text{ XOR } E_{com} = (e_m^3 \cdots e_1^3 e_0^3), \quad (4)$$

where $m = \lceil \frac{k}{3} \rceil - 1$ and $\lceil \cdot \rceil$ denotes the usual ceiling function. The definitions of the bitwise logical “AND” and “XOR” operators are presented in **Table 1** of [6]. Next, let the bit strings $E_{com[1]} = (e_m^{0[1]} \cdots e_1^{0[1]} e_0^{0[1]})$ and $E_{com[-1]} = (e_m^{0[-1]} \cdots e_1^{0[-1]} e_0^{0[-1]})$ separately store all bits of 1 and all bits of -1 in the bit string E_{com} . Similarly, let the bit strings $E'_{i[1]} = (e_m^{i[1]} \cdots e_1^{i[1]} e_0^{i[1]})$ and $E'_{i[-1]} = (e_m^{i[-1]} \cdots e_1^{i[-1]} e_0^{i[-1]})$ separately store all bits of 1 and all bits of -1 in the corresponding bit strings E'_i for $i = 1, 2, 3$.

Step 3. Use the so-called improved CMM–MSD Montgomery algorithm described in Section 3.4 of [6] to compute the values $M^{E_{com[1]}} \pmod{N}$, $M^{-E_{com[-1]}} \pmod{N}$, $M^{E'_{i[1]}} \pmod{N}$, and $M^{-E'_{i[-1]}} \pmod{N}$, for $i = 1, 2, 3$.

Step 4. Compute the intermediate exponentiation values as:

$$M^{E_i} \pmod{N} = M^{E_{com[1]}} M^{E'_{i[1]}} (M^{-E_{com[-1]}} M^{-E'_{i[-1]}})^{-1} \pmod{N} \text{ for } i = 1, 2, 3. \quad (5)$$

Step 5. The modular exponentiation $M^E \pmod{N}$ can be calculated as follows:

$$M^E = M^{E_1 \| E_2 \| E_3} = ((M^{E_1})^{2^{m+1}} (M^{E_2}))^{2^{m+1}} M^{E_3} \pmod{N}. \quad (6)$$

For efficiency evaluation, the improved CMM–MSD Montgomery algorithm mentioned in **Step 3** can be rewritten as **Fig. 1**. Here, MMR() denotes the CMM Montgomery method [4].

3. Computational efficiency of Wu's method

3.1. Preliminaries

Let $\Pr(EV)$ denote the probability that the event EV occurs. There is a well-known property of the MSD representation [1] as follows.

Algorithm 1

INPUT : $M, N, R = b^n \pmod{N}$, $E_{com[1]} = (e_m^{0[1]} \cdots e_1^{0[1]} e_0^{0[1]})$, $E_{com[-1]} = (e_m^{0[-1]} \cdots e_1^{0[-1]} e_0^{0[-1]})$,
 $E'_{i[1]} = (e_m^{i[1]} \cdots e_1^{i[1]} e_0^{i[1]})$ and $E'_{i[-1]} = (e_m^{i[-1]} \cdots e_1^{i[-1]} e_0^{i[-1]})$ for $i = 1, 2, 3$.
 OUTPUT : $C_0 = M^{E_{com[1]}} \pmod{N}$, $D_0 = M^{-E_{com[-1]}} \pmod{N}$, $C_i = M^{E'_{i[1]}} \pmod{N}$, and
 $D_i = M^{-E'_{i[-1]}} \pmod{N}$, for $i = 1, 2, 3$.
 Step A1-1 : $C_0 = C_1 = C_2 = C_3 = D_0 = D_1 = D_2 = D_3 = R \pmod{N}$, $S = MR \pmod{N}$;
 Step A1-2 : for $i = 0$ to m do {
 Step A1-2.1 : if $e_i^{0[1]} = 1$ then $C_0 = \text{MMR}(C_0 S)$; // evaluate $M^{E_{com[1]}} \pmod{N}$
 Step A1-2.2 : if $e_i^{0[-1]} = -1$ then $D_0 = \text{MMR}(D_0 S)$; // evaluate $M^{-E_{com[-1]}} \pmod{N}$
 Step A1-2.3 : if $e_i^{1[1]} = 1$ then $C_1 = \text{MMR}(C_1 S)$; // evaluate $M^{E'_{1[1]}} \pmod{N}$
 Step A1-2.4 : if $e_i^{1[-1]} = -1$ then $D_1 = \text{MMR}(D_1 S)$; // evaluate $M^{-E'_{1[-1]}} \pmod{N}$
 Step A1-2.5 : if $e_i^{2[1]} = 1$ then $C_2 = \text{MMR}(C_2 S)$; // evaluate $M^{E'_{2[1]}} \pmod{N}$
 Step A1-2.6 : if $e_i^{2[-1]} = -1$ then $D_2 = \text{MMR}(D_2 S)$; // evaluate $M^{-E'_{2[-1]}} \pmod{N}$
 Step A1-2.7 : if $e_i^{3[1]} = 1$ then $C_3 = \text{MMR}(C_3 S)$; // evaluate $M^{E'_{3[1]}} \pmod{N}$
 Step A1-2.8 : if $e_i^{3[-1]} = -1$ then $D_3 = \text{MMR}(D_3 S)$; // evaluate $M^{-E'_{3[-1]}} \pmod{N}$
 Step A1-2.9 : $S = \text{MMR}(SS)$; }
 Step A1-3 : $C_0 = \text{MMR}(C_0)$, $D_0 = \text{MMR}(D_0)$, $C_1 = \text{MMR}(C_1)$, $D_1 = \text{MMR}(D_1)$,
 $C_2 = \text{MMR}(C_2)$, $D_2 = \text{MMR}(D_2)$, $C_3 = \text{MMR}(C_3)$, $D_3 = \text{MMR}(D_3)$;
 Step A1-4 : Return $(C_0, D_0, C_1, D_1, C_2, D_2, C_3, D_3)$.

Fig. 1. Improved Montgomery modular exponentiation algorithm.

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