

# On elimination of inactive phosphorus in industrial POCl<sub>3</sub> diffused emitters for high efficiency silicon solar cells



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## ABSTRACT

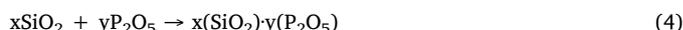
It is compelling to eliminate inactive phosphorus in industrial POCl<sub>3</sub> diffused emitters for high efficiency silicon solar cells. Several approaches were reported to significantly reduce inactive phosphorus in the literature. However, the underlying physical mechanisms are not fully understood. In this work, we conduct a systematic investigation and fabricate eight emitters involving two POCl<sub>3</sub>:O<sub>2</sub> ratios at pre-deposition and both inert and oxidising scenarios at drive-in. Characterization techniques such as secondary ion mass spectrometry and X-ray photoelectron spectroscopy are carried out with an emphasis on the phosphosilicate glass. To provide a consistent interpretation of our results and those in the literature, we suggest a probable reaction, “free phosphorus oxidation”, as the dominant mechanism to unveil the role and impact of oxygen during POCl<sub>3</sub> diffusion. We further propose that various industrial POCl<sub>3</sub> emitters can be fabricated inactive phosphorus free without drive-in or a pre-grown oxide. Among the eight emitters, three emitters are free from inactive phosphorus with a sheet resistance range from 65 to 140 Ω/□, featuring a surface doping from 1 × 10<sup>20</sup> to 3 × 10<sup>20</sup> cm<sup>-3</sup> and a remarkably low emitter saturation current density down to 51 fA/cm<sup>2</sup>.

## 1. Introduction

For *p*-type silicon (Si) substrates, the chosen phosphorus (P) diffusion technology has a big impact on the solar cell performance. Due to the cost effectiveness, POCl<sub>3</sub> tube diffusion process is the dominant industrial P diffusion technology to fabricate *n*-type emitters [1]. During the process, both phosphosilicate glass (PSG) deposition and P diffusion are performed inside one quartz tube at elevated temperatures. PSG deposition is accomplished by exposing the Si surface to a gas mixture of POCl<sub>3</sub> (carried by nitrogen) and oxygen (O<sub>2</sub>) to grow a PSG layer at about 800–900 °C. P diffusion takes place simultaneously with PSG deposition and even afterwards at an elevated temperature. The following three chemical reactions are typically used to describe POCl<sub>3</sub> diffusion [2–4]:



With the presence of SiO<sub>2</sub> and P<sub>2</sub>O<sub>5</sub> under an elevated temperature, both materials react and form PSG of different compositions [2,5–7].



As P diffusivity in Si scales up exponentially with temperature [8], a two-step diffusion approach is typically adopted, where PSG deposition is carried out at a rather low temperature (about 800 °C) to minimise P diffusion (“pre-deposition step”), while subsequently, POCl<sub>3</sub> supply is stopped and significant P diffusion takes place at a rather high temperature (“drive-in step”). In contrast, both PSG deposition and P diffusion occur under the same constant gas flow and medium temperature in a one-step diffusion approach. To give a clear overview of the POCl<sub>3</sub> diffusion process, a schematic temperature-time profile as well as gas flow-time profile for both the one- and two-step diffusion are shown in Fig. 1.

Due to the relative simplicity, screen printing technology is the most mature choice of industrial metallization for contacting POCl<sub>3</sub> diffused emitters. To ensure a low contact resistance of the screen-printed contact, a high active P concentration at the surface above 3.5 × 10<sup>20</sup> cm<sup>-3</sup> was a prerequisite for industrial silicon solar cells fabricated in the last few decades [9]. This requirement unfortunately resulted in severe recombination losses in the POCl<sub>3</sub> diffused emitters. Firstly, Auger recombination is the dominant loss [10] as it approximately scales with the square of the P concentration [11,12]. Secondly, surface recombination was also found to increase with the surface P

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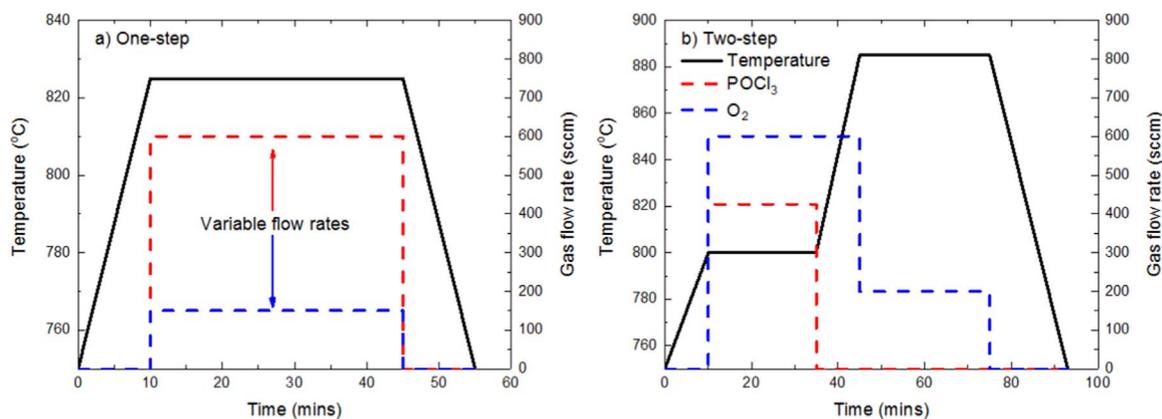


Fig. 1. Illustration of a  $\text{POCl}_3$  time-temperature/gas flow rate profile for emitter formation in both the one-step (a) and two-step (b) diffusion.

concentration [13–16]. Thirdly, such a high P concentration requires diffusing more P atoms into silicon, typically beyond the P solid solubility limit [17]. As a result, a “dead layer” is produced where many P atoms are electrically inactive [18–20]. These inactive dopants present in the form of interstitials, clusters [21,22] and  $\text{SiP}$  precipitates [5,23–25] and behave as Shockley-Read-Hall (SRH) [26,27] recombination centres [28].

Fortunately this requirement was relaxed lately as the screen printing technology advances. It was reported that a reasonable low contact resistance of about  $5 \text{ m}\Omega \text{ cm}^2$  was achieved on a  $120 \Omega/\square$  phosphorus emitter with a surface concentration of  $1.7 \times 10^{20} \text{ cm}^{-3}$  [29]. As a result, the intrinsic Auger recombination was no longer the dominant loss while the SRH recombination due to these inactive P atoms was identified as the major limitation of the industrial  $\text{POCl}_3$  emitter performance [20,30]. It is compelling to reduce/eliminate inactive P for high efficiency silicon solar cells. Substantial efforts were dedicated to fine tune  $\text{POCl}_3$  diffusion parameters in the past few decades and may be classified into three approaches: (a) pre-oxidation where an oxide layer is grown prior to  $\text{POCl}_3$  diffusion [31,32]; (b) lowering the  $\text{POCl}_3:\text{O}_2$  ratio [5,18,32–35]; and (c) post-oxidation where drive-in is performed under an oxidising ambient [36–42].

In the pre-oxidation and post-oxidation approaches, an oxide layer is grown on the Si surface due to Reaction (2). In lowering the  $\text{POCl}_3:\text{O}_2$  ratio, an oxide layer was also identified between the PSG and Si at pre-deposition [33], which holds Reactions (2) and (3) responsible. It appears that all these approaches make use of oxide for inactive P reduction. Since oxide was found to retard the diffusion of certain impurities such as P long ago [6,43], it may be rational to interpret the underlying mechanism as the diffusion barrier theory [35]. While this theory may explain some experimental results straightforwardly, we find that it is also inconsistent with some recent in-depth studies, where a thinner oxide was actually found to be associated with a lower  $\text{POCl}_3:\text{O}_2$  ratio [33,34]. It is contradictory for the diffusion barrier theory to attribute the less severe inactive P to the thinner oxide.

It is crucial to comprehend the right physical mechanisms behind these approaches in order to design various inactive-P-free industrial

emitters for high efficiency silicon solar cells. In this study, we carry out a systematic in-depth study with eight process splits, including two  $\text{POCl}_3:\text{O}_2$  ratios, three different temperatures and both inert and oxidising scenarios at drive-in. To unveil the underlying mechanisms, several characterization techniques are applied to all emitters including the electrochemical capacitance-voltage (ECV) technique and the secondary ion mass spectrometry (SIMS). Two emitters are further analysed with the X-ray photoelectron spectroscopy (XPS) technique with a special attention to the elemental distribution in PSG.

## 2. Experimental

### 2.1. Sample preparation

The samples used in this work were polished 4” round float-zone (FZ) grown *p*-type silicon wafers with a nominal bulk resistivity of  $2.4 \Omega \text{ cm}$  and a thickness of  $260 \mu\text{m}$ . After the RCA1 ( $\text{NH}_4\text{OH}:\text{H}_2\text{O}_2:\text{H}_2\text{O} = 1:1:5$ ) and RCA2 ( $\text{HCl}:\text{H}_2\text{O}_2:\text{H}_2\text{O} = 1:1:5$ ) cleaning procedure followed by a final HF dip (2.5% v/v), the wafers were loaded into a quartz tube diffusion furnace (TS8603, Tempres) at  $750^\circ\text{C}$  to start the  $\text{POCl}_3$  diffusion process. Eight diffusion splits were performed to prepare emitters in either one-step or two-step manner, as listed in Table 1. The temperature ramp rate was set to  $10^\circ\text{C}/\text{min}$  for all the heating and cooling steps. After removing the PSG in 2% diluted HF, the samples had a symmetrical  $n^+pn^+$  structure and were symmetrically coated by 100 nm silicon nitride ( $\text{SiN}_x$ ) film with a refractive index of 2.05 using plasma-enhanced chemical vapour deposition (PECVD, MAia XS, Meyer Burger) for lifetime measurement.

### 2.2. Characterization

Sheet resistance  $\rho_{sh}$  values were measured at 25 points equally spaced over the wafer by a four-point-probe scanner (SheRRescan, Mechatronics). An ECV analyser (CVP21, WEP) was used to measure the electrically active P doping profiles of the centre point of each wafer, calibrated by the  $\rho_{sh}$  data near the test location. Time-of-flight

Table 1

Process conditions for eight distinct emitters. The gas flow unit is standard cubic centimetres per minute (sccm).

Steps	Emitters Process parameters	A One-step	B	C	D Two-step	E	F	G	H
PSG deposition	Temperature ( $^\circ\text{C}$ )	825			795				
	Duration (min)	65	35	65	25 min				
	$\text{POCl}_3:\text{O}_2$ ratio (sccm:sccm)	600:150		450:600	600:150	450:600			
Ramp-up Drive-in	$\text{O}_2$ flow rate (sccm)	NA			0	0	450	600	600
	Temperature ( $^\circ\text{C}$ )				885				
	Duration (min)				30				
	$\text{O}_2$ flow rate (sccm)				0	0	0	0	160

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