

Accepted Manuscript

Predictive Runtime Verification of Timed Properties

Srinivas Pinisetty, Thierry Jéron, Stavros Tripakis, Yliès Falcone,
Hervé Marchand, Viorel Preteasa

PII: S0164-1212(17)30131-0
DOI: [10.1016/j.jss.2017.06.060](https://doi.org/10.1016/j.jss.2017.06.060)
Reference: JSS 9991



To appear in: *The Journal of Systems & Software*

Received date: 31 July 2016
Revised date: 25 January 2017
Accepted date: 19 June 2017

Please cite this article as: Srinivas Pinisetty, Thierry Jéron, Stavros Tripakis, Yliès Falcone, Hervé Marchand, Viorel Preteasa, Predictive Runtime Verification of Timed Properties, *The Journal of Systems & Software* (2017), doi: [10.1016/j.jss.2017.06.060](https://doi.org/10.1016/j.jss.2017.06.060)

This is a PDF file of an unedited manuscript that has been accepted for publication. As a service to our customers we are providing this early version of the manuscript. The manuscript will undergo copyediting, typesetting, and review of the resulting proof before it is published in its final form. Please note that during the production process errors may be discovered which could affect the content, and all legal disclaimers that apply to the journal pertain.

Highlights

- Introduce a predictive runtime verification (RV) mechanism for timed properties.
- Formally define the predictive runtime verification problem for timed properties.
- Algorithm to synthesize predictive RV monitors from timed properties.
- Implementation of the algorithm and its evaluation illustrating practicality.

ACCEPTED MANUSCRIPT

Download English Version:

<https://daneshyari.com/en/article/4956395>

Download Persian Version:

<https://daneshyari.com/article/4956395>

[Daneshyari.com](https://daneshyari.com)