## Accepted Manuscript

Bio-inspired Self-Aware Fault-Tolerant Routing Protocol for Network-on-Chip Architectures using Particle Swarm Optimization

Sani Abba, Jeong-A Lee

 PII:
 S0141-9331(17)30199-0

 DOI:
 10.1016/j.micpro.2017.04.003

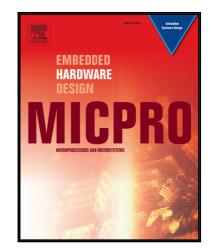
 Reference:
 MICPRO 2531

To appear in: Microprocessors and Microsystems

Received date:17 February 2016Revised date:25 February 2017Accepted date:6 April 2017

Please cite this article as: Sani Abba , Jeong-A Lee , Bio-inspired Self-Aware Fault-Tolerant Routing Protocol for Network-on-Chip Architectures using Particle Swarm Optimization, *Microprocessors and Microsystems* (2017), doi: 10.1016/j.micpro.2017.04.003

This is a PDF file of an unedited manuscript that has been accepted for publication. As a service to our customers we are providing this early version of the manuscript. The manuscript will undergo copyediting, typesetting, and review of the resulting proof before it is published in its final form. Please note that during the production process errors may be discovered which could affect the content, and all legal disclaimers that apply to the journal pertain.



## Highlights

- A routing scheme and fault-tolerant mechanism for the NoC architecture using a PSO methodology is proposed. This scheme uses the synchronous, asynchronous, and self-organizational features of PSO to create a minimal global routing table and intelligent adaptation, which gives rise to scalable, real-time and dynamic routing decisions with high throughput, low latency, and minimum power consumption. An experimental and simulation process for the proposed routing scheme is provided.
- A network routing performance evaluation of the proposed scheme compared with stateof-the-art fault-tolerant routing is provided. BISFTRP demonstrates better routing performance under faulty conditions, compared with state-of-the-art routing protocols.
- BISFTRP router hardware design and implementation results are provided. Postimplementation results show that BISFTRP router has an efficient logic area and power consumption compared with state-of-the-art routers.
- We demonstrate that the proposed routing scheme substantially increases network performance and provides efficient logic area and power consumption.

Download English Version:

## https://daneshyari.com/en/article/4956720

Download Persian Version:

https://daneshyari.com/article/4956720

Daneshyari.com