Accepted Manuscript

Computing Reliability: On the Differences between Software Testing and Software Fault Injection Techniques

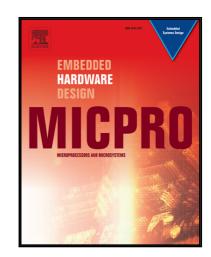
Maha Kooli, Firas Kaddachi, Giorgio Di Natale, Alberto Bosio, Pascal Benoit, Lionel Torres

PII: S0141-9331(16)30257-5 DOI: 10.1016/j.micpro.2017.02.007

Reference: MICPRO 2515

To appear in: Microprocessors and Microsystems

Received date: 13 October 2016 Accepted date: 24 February 2017



Please cite this article as: Maha Kooli, Firas Kaddachi, Giorgio Di Natale, Alberto Bosio, Pascal Benoit, Lionel Torres, Computing Reliability: On the Differences between Software Testing and Software Fault Injection Techniques, *Microprocessors and Microsystems* (2017), doi: 10.1016/j.micpro.2017.02.007

This is a PDF file of an unedited manuscript that has been accepted for publication. As a service to our customers we are providing this early version of the manuscript. The manuscript will undergo copyediting, typesetting, and review of the resulting proof before it is published in its final form. Please note that during the production process errors may be discovered which could affect the content, and all legal disclaimers that apply to the journal pertain.

ACCEPTED MANUSCRIPT

Computing Reliability: On the Differences between Software Testing and Software Fault Injection Techniques

Maha Kooli, Firas Kaddachi, Giorgio Di Natale, Alberto Bosio, Pascal Benoit, Lionel Torres

 $Laboratoire\ d'Informatique,\ de\ Robotique\ et\ de\ Microelectronique\ de\ Montpellier\ (LIRMM),\\ France\\ E-mail:\ name.surname@lirmm.fr$

Abstract

System reliability has become a main concern during the computer-based system design process. It is one of the most important characteristics of the system quality. The continuous increase of the system complexity make the reliability evaluation extremely costly. Therefore, there is need to develop new methods with less cost and effort. Furthermore, the system is vulnerable to both software and hardware faults. While the software faults are usually introduced by the programmer either at the design or the implementation stage of the software, the hardware faults are caused by physical phenomena affecting the hardware components, such as environmental perturbations, manufacturing defects, and aging-related phenomena. The software faults can only impact the software components. However, the hardware faults can propagate through the different system layers, and affect both the hardware and the software. This paper discusses the differences between the software testing and the software fault injections techniques used for reliability evaluation. We describe the mutation analysis as a method mainly used in software testing. Then, we detail the fault injection as a technique to evaluate the system reliability. Finally, we discuss how to use software mutation analysis in order to evaluate, at software level, the system reliability against hardware faults. The main advantage of this technique is its usability at early design stage of the system, when the instruction set architecture is not available. Experimental results run to evaluate faults occurring the memory show that the proposed approach significantly reduces the complexity of the system reliability evaluation in terms of time and cost.

Keywords: System Reliability, Hardware, Software, Mutation Testing, Fault Injection, Soft Errors.

1. Introduction

Reliability is one of the most important characteristics of the system qual-

Preprint submitted to Journal of LATEX Templates

February 28, 2017

Download English Version:

https://daneshyari.com/en/article/4956757

Download Persian Version:

https://daneshyari.com/article/4956757

<u>Daneshyari.com</u>