## Author's Accepted Manuscript

Bio-inspired Fault Tolerant Network on Chip

Muhammad Athar Javed Sethi, Fawnizu Azmadi Hussin, Nor Hisham Hamid



PII: S0167-9260(17)30242-0

DOI: http://dx.doi.org/10.1016/j.vlsi.2017.04.004

**VLSI1331** Reference:

To appear in: Integration, the VLSI Journal

Received date: 19 October 2016 Revised date: 24 March 2017 Accepted date: 19 April 2017

Cite this article as: Muhammad Athar Javed Sethi, Fawnizu Azmadi Hussin and Nor Hisham Hamid, Bio-inspired Fault Tolerant Network on Chip, Integration the VLSI Journal, http://dx.doi.org/10.1016/j.vlsi.2017.04.004

This is a PDF file of an unedited manuscript that has been accepted fo publication. As a service to our customers we are providing this early version o the manuscript. The manuscript will undergo copyediting, typesetting, and review of the resulting galley proof before it is published in its final citable form Please note that during the production process errors may be discovered which could affect the content, and all legal disclaimers that apply to the journal pertain

## ACCEPTED MANUSCRIPT

#### **Bio-inspired Fault Tolerant Network on Chip**

Muhammad Athar Javed Sethi\*<sup>1</sup>, Fawnizu Azmadi Hussin<sup>2</sup>, Nor Hisham Hamid<sup>2</sup>

atharsethi@uetpeshawar.edu.pk, {fawnizu, hishmid}@utp.edu.my

#### Abstract:

The integration of heterogeneous processing elements (PEs) or nodes in the System on Chip (SoC) has made the communication structure very complex. The bus based system between these components is not able to handle the communication requirements and, this has led to the idea of Network on Chip (NoC). The NoC addresses the communication requirement of different nodes on SoC. The physical sizes of devices in NoC are scaled down, including routers, processing elements and interconnects, giving rise to faults, system delay, and latency issues. Fault tolerant routing algorithms are used to recover from temporary faults while redundant resources (wires, routers) are required to overcome the permanent faults. These routing algorithms, however, still suffer from congestion problems, low bandwidth, and throughput utilization as well as lacking adaptivity and robustness. In this work, novel biologically inspired techniques were proposed for NoC using combined best effort (BE) and guaranteed throughput (GT) services. Moreover, the bio-inspired algorithms are compared and analyzed with each other using BE, GT and combined BE-GT services. The bio-inspired mechanisms of "synaptogenesis" and "sprouting" have been adopted in the proposed NoC algorithms and architecture. These techniques were implemented using the BE and GT services. With the help of these two bio-inspired techniques, the NoC becomes robust, fault tolerant and is able to efficiently utilize the throughput and bandwidth. The bio-inspired algorithms improved the accepted traffic (flit/cycle/node) by 38.99% compared to different techniques in the literature. The bio-inspired algorithm also improved the bandwidth and throughput utilization by 71.04% and 72.42% respectively compared to the XY and Odd-Even fault tolerant routing algorithms. Moreover, the bio-inspired algorithm had less end-toend latency and interflit arrival time by 196.44% and 88.10% respectively compared to the literature techniques of XY and Odd-Even.

**Keywords:** Network on Chip (NoC); Fault tolerant algorithms; Bio-inspired solutions; Quality of Service (QoS)

<sup>&</sup>lt;sup>1</sup>Department of Computer Systems Engineering, University of Engineering and Technology, Peshawar, Pakistan. (Tel: +923340012468)

<sup>&</sup>lt;sup>2</sup>Department of Electrical & Electronics Engineering, Universiti Teknologi PETRONAS, Tronoh, Perak, Malaysia.

### Download English Version:

# https://daneshyari.com/en/article/4970672

Download Persian Version:

https://daneshyari.com/article/4970672

<u>Daneshyari.com</u>