



## Research Article

## Enhanced cascade control for a class of integrating processes with time delay

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## ABSTRACT

Unlike self-regulating processes, cascade control strategies for control of integrating processes with time delay are limited. A novel series cascade control structure to enhance the closed loop performance is proposed for integrating and time delay processes. The proposed controller structure has only two controllers and a setpoint filter. The inner loop controller is designed based on IMC approach and the primary setpoint filter is based on optimal performance index. The primary load disturbance rejection controller, a PID controller in series with a lead/lag compensator, is designed on the basis of the desired closed-loop complementary sensitivity function. The robustness analysis is carried out using Kharitonov's theorem. Simulation results demonstrate the efficacy of the proposed method by showing satisfactory nominal and robust performances.

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## 1. Introduction

A cascade control (which was first introduced by [1]) is a multi-loop control scheme commonly used in chemical process control. It is used when processes are difficult to control due to large disturbances and load changes, and the control quality is to be maintained at a high level. The traditional cascade control structure basically consists of two loops: the outer (primary or master) and the inner (secondary or slave) loops. PID controllers are still widely used in process industries even though more advanced control techniques have been developed. Many approaches have been used to determine the PID controller parameters for a single-loop system but few investigations have been made on tuning PID controllers for a cascade system. The design and analysis of cascade control strategies for stable processes are addressed by many researchers such as [2–15]. However, limited research has been carried out for the design of cascade controllers for integrating processes. The control of integrating processes is much more difficult than the control of self-regulating processes. Again, a simple cascade control structure may not give a satisfactory regulatory performance for integrating processes if the time delay incorporated is dominant. It is widely known that the process time delay can be compensated effectively by the use of Smith predictor. A control structure which possesses the features of both cascade control and the Smith predictor together can drastically improve the closed-loop performances. Recently, Kaya and Atherton [16] have proposed a

control structure for controlling integrating processes using modified Smith predictor, but their control structure involves four controllers. Later on Uma et al. [17] proposed a control structure for integrating processes with three controllers and a filter. In the present work, a modified Smith predictor [18] is used in the outer loop of the cascade control structure for control of integrating processes with time delay. This paper shows how the proposed cascade control enhances the closed-loop performances with only two controllers and a filter. The two main advantages of the proposed control structure are: firstly, it suppresses the load disturbance and compensates the dead time and secondly, the servo response decouples the regulatory response in nominal case. Furthermore, to improve the practical utility of cascade control structures, we conduct a more in-depth analysis of the effect of load disturbances on closed-loop performance with the help of simulation tool. This will provide support for control engineers in designing more effective strategies for multi-loop or MIMO (multi-input and multi-output) control systems.

For clear interpretation, the proposed cascade control structure is presented in Section 2. The controller design procedures are given in Section 3. Selection of tuning parameters is addressed in Section 4 followed by robustness analysis in Section 5 and the simulation results in Section 6. The conclusions are drawn in Section 7.

## 2. Series cascade control structure

The proposed series cascade control structure for integrating processes with time delay is shown in Fig. 1.  $G_{p1}$  and  $G_{p2}$  are the

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