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# Thin titanium oxide films deposited by e-beam evaporation with additional rapid thermal oxidation and annealing for ISFET applications

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#### ABSTRACT

Titanium oxide (TiO<sub>2</sub>) has been extensively applied in the medical area due to its proved biocompatibility with human cells [1]. This work presents the characterization of titanium oxide thin films as a potential dielectric to be applied in ion sensitive field-effect transistors. The films were obtained by rapid thermal oxidation and annealing (at 300, 600, 960 and 1200 °C) of thin titanium films of different thicknesses (5 nm, 10 nm and 20 nm) deposited by e-beam evaporation on silicon wafers. These films were analyzed as-deposited and after annealing in forming gas for 25 min by Ellipsometry, Fourier Transform Infrared Spectroscopy (FTIR), Raman Spectroscopy (RAMAN), Atomic Force Microscopy (AFM), Rutherford Backscattering Spectroscopy (RBS) and Ti-K edge X-ray Absorption Near Edge Structure (XANES). Thin film thickness, roughness, surface grain sizes, refractive indexes and oxygen concentration depend on the oxidation and annealing temperature. Structural characterization showed mainly presence of the crystalline rutile phase, however, other oxides such Ti<sub>2</sub>O<sub>3</sub>, an interfacial SiO<sub>2</sub> layer between the dielectric and the substrate and the anatase crystalline phase of TiO2 films were also identified. Electrical characteristics were obtained by means of I-V and C-V measured curves of Al/Si/TiO<sub>x</sub>/Al capacitors. These curves showed that the films had high dielectric constants between 12 and 33, interface charge density of about 10<sup>10</sup>/  $cm^2$  and leakage current density between 1 and  $10^{-4}$  A/cm<sup>2</sup>. Field-effect transistors were fabricated in order to analyze  $I_{\rm D} \times V_{\rm DS}$  and  $\log I_{\rm D} \times {\rm Bias}$  curves. Early voltage value of  $-1629 \, {\rm V}$ ,  $R_{\rm OUT}$  value of 215 M $\Omega$  and slope of 100 mV/dec were determined for the 20 nm TiO<sub>x</sub> film thermally treated at 960 °C. © 2009 Elsevier B.V. All rights reserved.

#### 1. Introduction

Health-care industry, food quality appraisal and food monitoring are responsible for the ever increasing research and development in the field of biosensors. Especially in the medical area, biosensors represent an astonishing expanding field [1], and particularly an increasing interest is found in applications related to cell monitoring biosensors. In this field, ion sensitive field-effect transistors (ISFETs) have been explored since they have advantages such as very small size, rapid response and low output impedance, in addition to be compatible with CMOS process and have a low cost [2]. The concept of ISFET device is based on the metal-oxide-semiconductor field-effect transistor operating theory, where metal gate is replaced by a reference electrode, buffer solution and an ion sensitive membrane. As an ISFET is basically a type of MOSFET without the metal gate, the study of the insulator mate-

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rial is an important part of the process, because it is directly exposed to the buffer solution. Therefore, the insulator material will be responsible for the detection of the specimens since a change in the electrolyte pH concentration induces a change in the electrolyte and insulate surface potential that results in alterations in the insulator-semiconductor electric field interface, channel conductance and current modulation. Due to the interest in measuring biological specimens, a biocompatible gate dielectric is a requirement. As titanium oxide (TiO<sub>2</sub>) has been extensively applied in the medical area due to its proved biocompatibility with human cells [1] it becomes a potential gate dielectric material. It also has the advantage to be a material with high dielectric constant (k) which is a mandatory requirement for the sub-32 nm CMOS technology development. High-k insulators can present dielectric constant values between 4 and 80, which are higher than the 3.9 of silicon oxide. This characteristic allows reduction of tunneling current effect presented by ultra-thin silicon oxide gate insulators. Because of this interesting characteristic, deposition of TiO<sub>2</sub> films have been investigated by different techniques such as electron beam evaporation, ion sputtering, chemical vapor deposition, atomic layer deposition and sol-gel method [3-6]. This paper

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deals with  $TiO_x$  thin films obtained by rapid thermal oxidation (RTO) and annealing (RTA) of titanium thin films deposited on Si substrates by e-beam evaporation for future application as gate dielectrics in ISFETs device fabrication.

#### 2. Experimental procedure

Enhanced nMOSFETs were fabricated on n-type single-crystal Si (1 0 0) wafers with resistivity ranging from 1 to 10  $\Omega$  cm. These devices with areas from 10  $\mu m \times 100~\mu m$  to 200  $\mu m \times 200~\mu m$  and MOS capacitors with area of 200  $\mu m \times 200~\mu m$  were defined with five lithography steps. The substrates were cleaned by the RCA method between each process step and were oxidized (1000 °C for 280 min in  $O_2 + H_2 O(v)$ ) to grow a 1  $\mu m$  field silicon oxide. Phosphorus ion implantation (80 keV  $P^+$  ions and dose of  $7 \times 10^{15}~ions/cm^2$ ) and dopant activation annealing (1000 °C for 30 min in  $N_2$ ) were used to form source and drain junctions.

To distinguish gate titanium oxide formation methods, the samples were named Ti\_5 nm, Ti\_10 nm and Ti\_20 nm, the titanium oxide formed by rapid thermal oxidation and annealing for 40s at temperatures of 300, 600, 960 and 1200 °C of 5 nm, 10 nm and 20 nm of Ti deposited by e-beam evaporation. For the process control, bare Si substrates were oxidized and annealed in the same conditions by rapid thermal (RT) processes (omitting the Ti e-beam evaporation step) and were named Control (SiO $_2$ ).

To complete MOS capacitor and nMOSFET device fabrication, the source, drain, gate and body electrodes were formed by sputtering of a 300 nm thick aluminum film, sintered by conventional furnace in forming gas at 430 °C for 2, 5, 10, 15, 20 and 25 min.

FTIR spectra, used to reveal the titanium oxide film chemical bonds, were obtained using a Digilab Scimitar FTS 2000 Series FTIR Spectrometer and Raman spectroscopy by NTEGRA Spectra PNL. The thickness and refractive index were obtained using a Rudolph/AutoEL Technologies Inc., Ellipsometer.

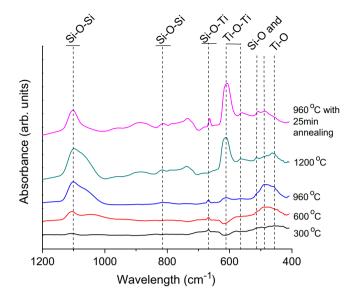
Ti–K edge XANES measurements were conducted at the Brazilian Synchrotron Light Laboratory (LNLS, Campinas, Brazil), performed at room temperature and were recorded in the 4900–5200 eV range.

Electrical properties of the Al/TiO $_x$ /Si/Al capacitors were investigated using high-frequency capacitance–voltage (C–V) and leakage current–voltage (I–V) measurements. The capacitance was measured at a frequency of 1 MHz at room temperature. A relative dielectric constant of 3.9 was adopted to extract the equivalent oxide thickness (EOT) of films from C–V curves under strong accumulation condition. nMOSFET electrical characteristics, such as flat-band voltages, threshold voltages and sub-threshold slopes were obtained.

#### 3. Results and discussion

Structural characterization of Ti\_5 nm, Ti\_10 nm and Ti\_20 nm titanium oxide films formed by rapid thermal oxidation and annealing at temperatures of 300, 600, 960 and 1200 °C, show that independently of the initial e-beam evaporated titanium thickness, all evaluated samples present the same structural characteristics. Figs. 1–5 show the results of the characterizations of Ti\_20 nm titanium oxide film thermally treated at the temperatures already described and Ti\_20 nm titanium oxide film thermally treated at 960 °C with additional annealing in forming gas for 25 min.

According to Fig. 1, FTIR analyses, every obtained spectra exhibit the presence of a low intensity absorbance peak at 513 cm $^{-1}$ . This peak is attributed to the Ti–O anatase crystal structure stretching vibrational mode [7]. With the increase of the process temperature this peak decreases significantly, and the peaks at  ${\sim}485$  and  ${\sim}615$  cm $^{-1}$  become more pronounced, which are usually



**Fig. 1.** FTIR spectra of  $Ti_20$  nm titanium oxide films thermally treated at 300, 600, 960, 1200 °C and titanium oxide films thermally treated at 960 °C with 25 min annealing at 430 °C in forming gas.

attributed to the stretching vibrational mode of a rutile crystal structure [8]. A peak at  $\sim$ 430 cm<sup>-1</sup> is related to Si-O rocking vibrational mode [9], however, as the absorbance peaks between 430 and 513 cm<sup>-1</sup> form a broad band, they cannot define the film crystal structure. Furthermore, according to Verma et al [7], there is also existence of a  $Ti_2O_3$  peak at  $\sim$ 480 cm<sup>-1</sup>, which means that anatase crystalline structure and Ti<sub>2</sub>O<sub>3</sub> may be present in the studied films. It is easy to note however, that increasing the process temperature, the absorbance peak at 615 cm<sup>-1</sup> becomes pronounced, confirming the existence of the rutile crystalline structure, especially after 25 min annealing at 430 °C in forming gas. Moreover, the samples exhibit peaks at  $\sim$ 810 and  $\sim$ 1100 cm<sup>-1</sup> assigned to bending and stretching vibrational modes of SiO<sub>2</sub>, respectively [9], indicating that all films showed formation of a SiO<sub>2</sub> layer between the TiO<sub>x</sub> film and the substrate. Finally, the peak found at  $\sim$ 664 cm<sup>-1</sup> is attributed to the presence of Ti–Si–O [9].

Fig. 2 shows the Raman spectra taken on these samples. In all spectra the presence of Raman shifts at 430, 612 and 826 cm $^{-1}$  are clearly identified, and they correspond to the rutile crystal structure [10]. However, similarly to the FTIR analyses, our samples also exhibited a Raman shift at 650 cm $^{-1}$  related to the anatase crystal structure [11,12]. Fig. 3, shows that Raman shift peak intensity of the Ti\_20 nm sample thermally treated in 960 °C increases when the samples are subjected to 25 min annealing at 430 °C in forming gas. The observed result indicates that even varying the process temperature it was not able to isolate a single-crystal structure of TiO2.

Furthermore, in regard to the microelectronics applications, it is not interesting to have a  ${\rm TiO_2}$  layer with crystalline structure, since this may leads to cause an increase in the leakage current of the devices [13]. Finally, the Raman analysis support the results of the Infrared Spectroscopy, since it also revealed the existence of anatase and rutile phases in the obtained films, but in both techniques rutile is the predominant crystalline structure.

Ti–K XANES analyses realized on the fabricated samples, Fig. 4, shows that the spectra of the films are similar to the spectra of the  ${\rm TiO_2}$  rutile type control, in all the used thermal treatment temperatures, which is in agreement with the results obtained by RAMAN and FTIR analyses.

The results obtained from Raman and FTIR measurements were also confirmed by RBS simulations, which considered Si/SiO<sub>2</sub>/

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