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endotaxial growth of CoSi₂ nanowires on Si(001) surface: The influence of surface reconstruction

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ABSTRACT

Evidence for the influence of Si(001)-(2 × 1) surface reconstruction on the elongation direction of $CoSi_2$ flat 13 islands is discussed in this paper. Step height analysis of these flat islands shows that flat island heights, H_A , follow 14 discrete values of N_A such that $H_A = mN_A + c$, where $N_A = 1, 2, 3, ..., m$ is equivalent to the number of 15 monoatomic step height (1.4 Å) of the Si(001) surface, and *c* is the initial island height when $N_A = 0$. The N_A 16 values were found to be correlated to the flat island elongation direction with respect to the (2 × 1) dimer 17 rows. For a given terrace, the preferred elongation direction of these flat islands is parallel to the Si dimer 18 rows. As a result, orthogonally elongated islands are clearly resolved on adjacent terraces, which are separated 19 by monoatomic steps. The endotaxial growth of these flat islands is thus also influenced by the anisotropic 20 adatom diffusion due to (2 × 1) surface reconstruction. 21

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34 1. Introduction

To achieve preferential growth and selective control over the 35 morphology of silicon -based heteroepitaxial low-dimensional nano-36 structures, such as nanowires, requires fundamental insights into the 37 growth dynamics and kinetics [1-3]. Homoepitaxial growth of Si nano-38 wires, for example, on Si(001)- (2×1) surface have shown to be influ-39 enced by surface reconstruction, where anisotropic Si-adatom diffusion 40 41 leads to growth of Si islands forming nanowires [4,5]. On the other hand, the formation of heteroepitaxial nanowires, such as rare-earth 42silicide system [6–9], have been attributed to anisotropic lattice 43mismatches between islands and substrates. These islands elongate 44 45 preferentially along the direction with lower lattice-mismatch in order to minimize strain energy. In other cases, the shape transition of compact 46 islands to nanowires driven by strain relaxation have also been reported 47 48 for material systems such as Ag nanowires [10] and silicide nanowires such as $CoSi_2$ on Si(001) [11–16]. The introduction of nanowires growing 49 epitaxially into the substrate surface (endotaxy) reviewed recently for 5051several systems such as Ni, Co, Pt, and Fe on Si(001), Si(110), and 52Si(111) surfaces, however, revealed a more complex mechanism for 53nanowire formation and growth [17–19].

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Of particular interest is the formation of CoSi₂ low-dimensional 54 structures. Recent works have shown that nanowires on Si(001) are $\ 55$ formed not only because of strain-induced shape transition [13,16] 56 but is also a consequence of a thermally activated growth due to 57 endotaxy [18,20-24]. It has been reported that CoSi2 forms two types 58 of islands, ridge and flat, on the Si(001) surface [23]. Briefly, ridge 59 islands form preferentially low energy CoSi₂{111}//Si{111} interface 60 (Type B) with Si, which appears to show a "twinned" type interface 61 due to a stacking fault. They grow endotaxially because the low energy 62 Type B interface is more energetically favorable than other higher ener- 63 getic planes along the <110 > directions (CoSi₂{111}//Si{115}, 64 CoSi₂{115}//Si{111}, and CoSi₂{112}//Si{112}). Its preferred growth 65 over other interfacial planes results in wire formation. For flat-type 66 islands, they form Type A, i.e., CoSi₂{111}//Si{111}, interface 67 endotaxially with Si. Type A interface, unlike Type B, is "untwinned" 68 where the lattice structure of CoSi₂ follows that of Si. These islands are 69 bound by the energetically favorable Type A interface on all sides, 70 resulting in the preferential formation of compact islands. However, 71 growth at low temperature results in wire-like flat islands before com-72 pact islands are seen at high temperatures. Although the occurrence of 73 wire-like features have been attributed to the presence of corner bar-74 riers [23,25,26], the influence of Si(001)- (2×1) surface reconstruction 75 due to the anisotropic diffusion of adatoms along and across dimer rows 76 has not been adequately addressed. Here, we report the anisotropic 77 growth of wire-like flat islands at low growth temperatures between 78 500 °C and 650 °C. By analyzing the island height with respect to the 79

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Si(001)- (2×1) reconstructed surface, we elucidate the apparent orthogonal elongation directions along <110> of flat nanowires as observed at low growth temperatures—a consequence which we attribute to the influence of surface reconstruction of Si(001).

84 2. Methods

The STM experiments were carried out in situ in an OMICRON Ultra-85 High Vacuum (UHV) system with base pressure of 2×10^{-10} Torr. 86 87 equipped with an OMICRON Variable Temperature-Scanning Tunneling Microscope (VT-STM). Si samples were cut from Boron-doped P-type 88 89 singular Si(001) wafers with resistivity less than 0.1 Ω /cm supplied by Virginia Semiconductors. These samples were chemically etched ex 90 situ based on the recipe described by Ong et al. [27,28]. They were 91 92then dipped in dilute aqueous hydrofluoric (HF) acid solution to terminate the surface with hydrogen prior to outgassing in the UHV chamber 93 for 8 h at ~300 °C. The samples were then progressively annealed in 94 steps of 50 °C to 700 °C and then flashed at 30 s per cycle to 1100-95 96 1150 °C. Upon cooling to room temperature, the clean samples' surface 97 morphologies were verified in situ using a VT-STM. The samples were 98 then deposited at elevated temperatures (530 °C, 560 °C, 590 °C, and 620 °C) with cobalt (Goodfellow, > 99.99 + % purity) using electron-99 beam evaporation with a rate of 0.1 ML/min for 1 min. Temperatures 100 and deposition rate were determined using an infrared pyrometer and 101 102 a quartz crystal monitor (QCM), respectively. The surface morphologies were subsequently characterized in situ using the VT-STM. The STM 103 tungsten tips were fabricated using the Omicron Tip-Etching kit and 104 then outgassed in UHV prior to STM scans. All STM images were taken 105 in situ at room temperature using constant-current mode, with a 106 tunneling current at -1.0 nA, sample bias of -2.0 V with acquisition 107 done in a unidirectional mode. Drift in between images were kept to a 108 minimum by allowing longer times for the STM tip to scan the surface. 109 The STM scan direction was kept unidirectional from bottom to top. 110 The STM images were background-corrected (planar) and flattened 111 ("Flatten discarding regions") using the WSxM software (Nanotec 112Electronica) [29]. Analyses and measurements from these images 113were done using the same software. 114

115 **3. Results and discussions**

116 3.1. Flat island height evolution and height analyses

Details of the structure of ridge-type and flat-type islands and their epitaxial relationships with the Si(001) substrate have been reported previously [23]. Fig. 1a shows a 500 nm × 500 nm global surface morphology of a Si(001) surface after 0.1 ML of cobalt is deposited at 530 °C. Si dimer 120 rows elongates along the [110] direction on Si(001)-(2 × 1) surface as 121 shown in the figure. The surface morphology consists only of two types 122 of islands: flat type (labeled "F") and ridge type (labeled "R"), elongating 123 along two orthogonal [110] and [110] directions. Apart from these islands, 124 the surface is also decorated with "holes" (labeled as "h") surrounding 125 several islands with remnant Si terraces exhibiting (1 × 2) reconstruction 126 (labeled as "t"). 127

Fig. 1b shows the average length to width aspect ratio of the flat 128 islands measured as a function of its growth temperature. The plot rep- 129 resents the shape evolution of the flat islands with increasing growth 130 temperature. Between 530 °C and 650 °C, the average length to width 131 aspect ratio is shown to increase rapidly from 2:1 to 7.5:1. Thereafter, 132 it decreases rapidly back to 2:1 above 710 °C. The increase in the aspect 133 ratio shows the elongation of flat islands at lower growth temperatures. 134 However, interfacial energies of the flat island are reported to be similar 135 on all sides of the island since they are bound at the $CoSi_2{111}//Si{111}$ 136 interface. The formation of flat islands energetically favors a compact 137 isotropic shape (e.g., square) rather than a nanowire. The elongation 138 of the flat island therefore suggests other factors which kinetically 139 limit the growth and formation of compact flat islands. We proceeded 140 to examine the flat islands in more detail and subsequently reveal the 141 correlation between the flat island's heights and their elongation direc- 142 tion with respect to the Si dimer rows. 143

Two flat islands from Fig. 1a (labeled as "2a" and "2b"), which 144 appear to elongate parallel and perpendicular to the Si(001)- 145 (2×1) dimer rows, were then scanned at higher resolution and 146 shown in Fig. 2a and b. The flat island in Fig 2a elongates along 147 the Si dimer rows with respect to the main Si(001)- (2×1) terrace 148 labeled "1," while it is found to elongate perpendicular to the adja- 149 cent remnant Si(001)- (1×2) terrace labeled "2." The line profile 150 AB shows that the island height is about 2.4 ± 0.2 Å with respect 151 to terrace "2," while the line profile CD shows that the height is 152 3.8 ± 0.2 Å with respect to terrace "1." Comparing the two line pro- 153 files, the height difference between the Si terraces "1" and "2" is 154 thus about 1.4 Å.

Fig. 2b shows the second flat island elongating along [110] per- 156 pendicular to the Si(001)-(2 × 1) Si dimer rows. The line profiles 157 EF and GH give the island a height of 3.7 ± 0.2 Å with respect to rem- 158 nant Si(001)-(1 × 2) terrace labeled "2" and 5.1 ± 0.2 Å with respect 159 to the main Si(001)-(2 × 1) terrace labeled "1." Comparing the two 160 line profiles EF and GH, the height difference between terrace "1" 161 and "2" is also about 1.4 Å. Both flat island measurements suggest 162 that their heights are closely correlated to the monoatomic step 163 height of the Si(001) surface (1.4 Å). The remnant Si terraces in 164

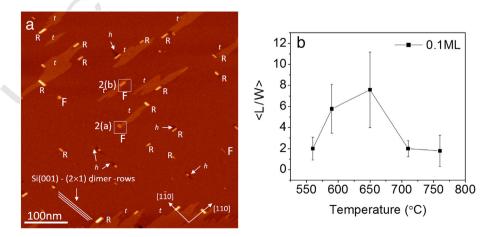


Fig. 1. (a) STM global morphologies of $Si(001)-(2 \times 1)$ deposited with 0.1 ML Co at 530 °C (500 nm × 500 nm). Some flat islands and ridge islands are labeled "F" and "R," respectively. Features labeled "h" are holes which formed as Si is consumed during cobalt deposition. Consumption of Si results in the remnant Si(001)-(1 × 2) terraces left after cobalt deposition (labeled as "t"). (b) Average length to width aspect ratio of flat islands measured as function of growth temperature.

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