

Microelectronics Journal 38 (2007) 606-609

Microelectronics Journal

www.elsevier.com/locate/mejo

Epitaxial lateral overgrowth of InP/GaAs (100) heterostructures by metalorganic chemical vapor deposition

Deping Xiong*, Xiaomin Ren, Qi Wang, Aiguang Ren, Jing Zhou, Jihe Lv, Shiwei Cai, Hui Huang, Yongqing Huang

Key Laboratory of Optical Communication and Lightwave Technologies, Ministry of Education, Beijing University of Posts and Telecommunications, Beijing 100876, PR China

> Received 28 December 2006; accepted 19 February 2007 Available online 16 April 2007

Abstract

Epitaxial lateral overgrowth (ELOG) was used to grow InP on GaAs(100) substrates by metalorganic chemical vapor deposition (MOCVD). The selectivity of InP by ELOG is excellent and the regrowth InP epilayers have good morphology without polycrystalline on SiO₂ mask. The [0 $\bar{1}$ 1] directional mask stripes and high V/III ratio are benefit to InP lateral growth. Compared to conventional direct growth, ELOG is effective in reducing the dislocation density, relaxing compressing strain in epilayers. In addition, the full width at half maximum (FWHM) of X-ray diffraction (XRD) ω scans and room temperature (RT) photoluminescence (PL) for a 3 μ m thick epilayer by ELOG are 198 arcsec and 44 meV, respectively.

Keywords: Epitaxial lateral overgrowth; MOCVD; X-ray diffraction; Scanning electronic microscopy

1. Introduction

The InP-based optoelectronic devices are essential for optical communication, while InP-based integrated circuit (IC) technology is still immature compared to that of GaAs IC. If the InP-related optical devices and GaAs electronic devices are combined on GaAs substrates, optoelectronic integrated circuits (OEIC) could be obtained [1]. Therefore, InP layers grown on GaAs substrates have attracted considerable attention [2–5]. In addition, large-area and low-cost GaAs substrates are available compared to InP substrates.

To overcome the 4% lattice mismatch of InP/GaAs heterostructure, two-step method [2], sawtooth-patterned substrates [3], strained-layer superlattice [6,7] and composition-graded [8,9] buffer layers were commonly adopted. Epitaxial lateral overgrowth (ELOG), a method to solve heteroepitaxial growth with large lattice mismatch, was widely used in GaN/sapphire [10–13], GaAs/Si [14,15], but

*Corresponding author. Tel.: +86106228400485;

fax: +86 10 62284004 88.

E-mail address: xiongdeping@gmail.com (D. Xiong).

it was seldom reported to be used in InP/GaAs heterostructures. ELOG is a kind of epitaxial growth on a partially masked substrate [16], the main advantage of this method is that the dislocations can be blocked by the mask, thus it is a promising tool to grow high-quality epilayers on lattice-mismatched substrates.

In this work, we attempted to use ELOG to grow InP/GaAs heterostructures by metalorganic chemical vapor deposition (MOCVD), the ways to improve the ratio of lateral to vertical growth rate were investigated, the epilayer quality was characterized by scanning electron microscopy (SEM), double crystal X-ray diffraction (XRD) and room temperature (RT) photoluminescence (PL).

2. Experimental procedure

Firstly 0.4 µm thick InP epilayers were grown on GaAs substrate by two-step method in a low-pressure MOCVD system [17]. Then the InP epilayers were coated with 200 nm SiO₂ using plasma-enhanced chemical vapor deposition (PECVD). Openings were defined using standard lithographic

procedures and SiO_2 was etched with hydrofluoric acid (HF). The pattern consisted of $5\,\mu m$ SiO_2 mask and $5\,\mu m$ InP window stripes alternately, which oriented along the $[0\ \bar{1}\ 1]$ direction or the $[0\ \bar{1}\ 1]$ direction, as shown in Fig. 1.

Lastly, the patterned substrates were cleaned in acetone and ethanol before loading to reactor chamber again.

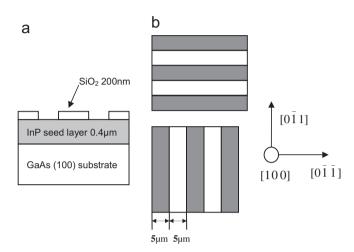


Fig. 1. (a) Cross-sectional and (b) surface schematic diagrams of InP/GaAs(100) substrate with SiO₂ mask stripes along the [0 $\bar{1}$ 1] direction or the [0 $\bar{1}$ $\bar{1}$] direction.

Subsequent regrowth was carried out at $685\,^{\circ}\text{C}$ and the nominal thickness was $3\,\mu\text{m}$, trimethyl-indium (TMIn) and phosphine (PH₃) were used as source materials and the V/III ratio was 200.

3. Result and discussion

Fig. 2 shows the SEM images of ELOG InP on GaAs substrates with the mask pattern shown in Fig. 1, among them, Fig. 2a is the surface image of Fig. 2b, it can be seen that the surface morphology is good and no cracks occur [10], in addition, there is no polycrystalline on the SiO₂ mask [10,18,19], which shows that the selectivity of InP grown by ELOG is excellent.

Fig. 2b–d shows the SEM cross section images of three samples with different growth conditions. The samples shown in Fig. 2b and c have the same SiO₂ mask stripe direction, i.e., along the [0 $\bar{1}$ 1] direction, they are taken from (0 $\bar{1}$ 1) cross section, but the sample shown in Fig. 2c has higher V/III ratio (250) than that in Fig. 2b (180) when the regrowth was carried out; both the two samples show a trapezoid shape in cross section, a higher V/III ratio would be benefit to lateral growth, since the largest width in Fig. 2b is 7.62 μ m compared to 7.84 μ m in Fig. 2c while they all have the same 5 μ m InP window width and total 3 μ m thickness, though the improvement is not obvious

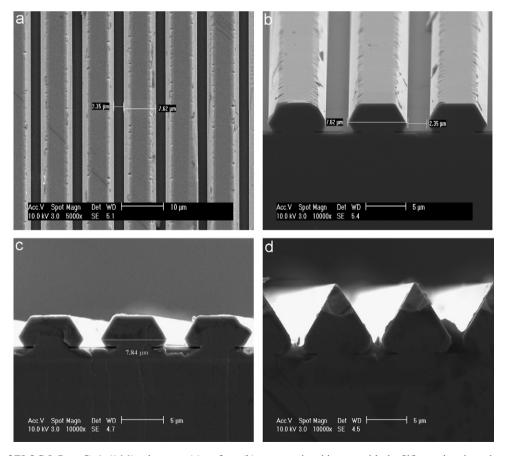


Fig. 2. SEM images of ELOG InP on GaAs(100) substrates: (a) surface; (b) cross-sectional images with the SiO₂ mask stripes along the $[0\ \bar{1}\ 1]$ direction, the V/III ratio is 180; and (c, d) cross-sectional images with the SiO₂ mask along the $[0\ \bar{1}\ 1]$ and $[0\ \bar{1}\ \bar{1}]$ direction, the V/III ratio is 250 and 180, respectively.

Download English Version:

https://daneshyari.com/en/article/546666

Download Persian Version:

https://daneshyari.com/article/546666

<u>Daneshyari.com</u>