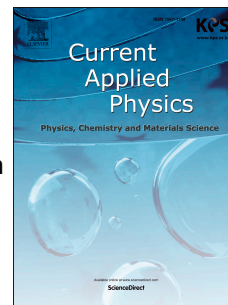


# Accepted Manuscript

Performance enhancement of micro-supercapacitor by coating of graphene on silicon nanowires at room temperature

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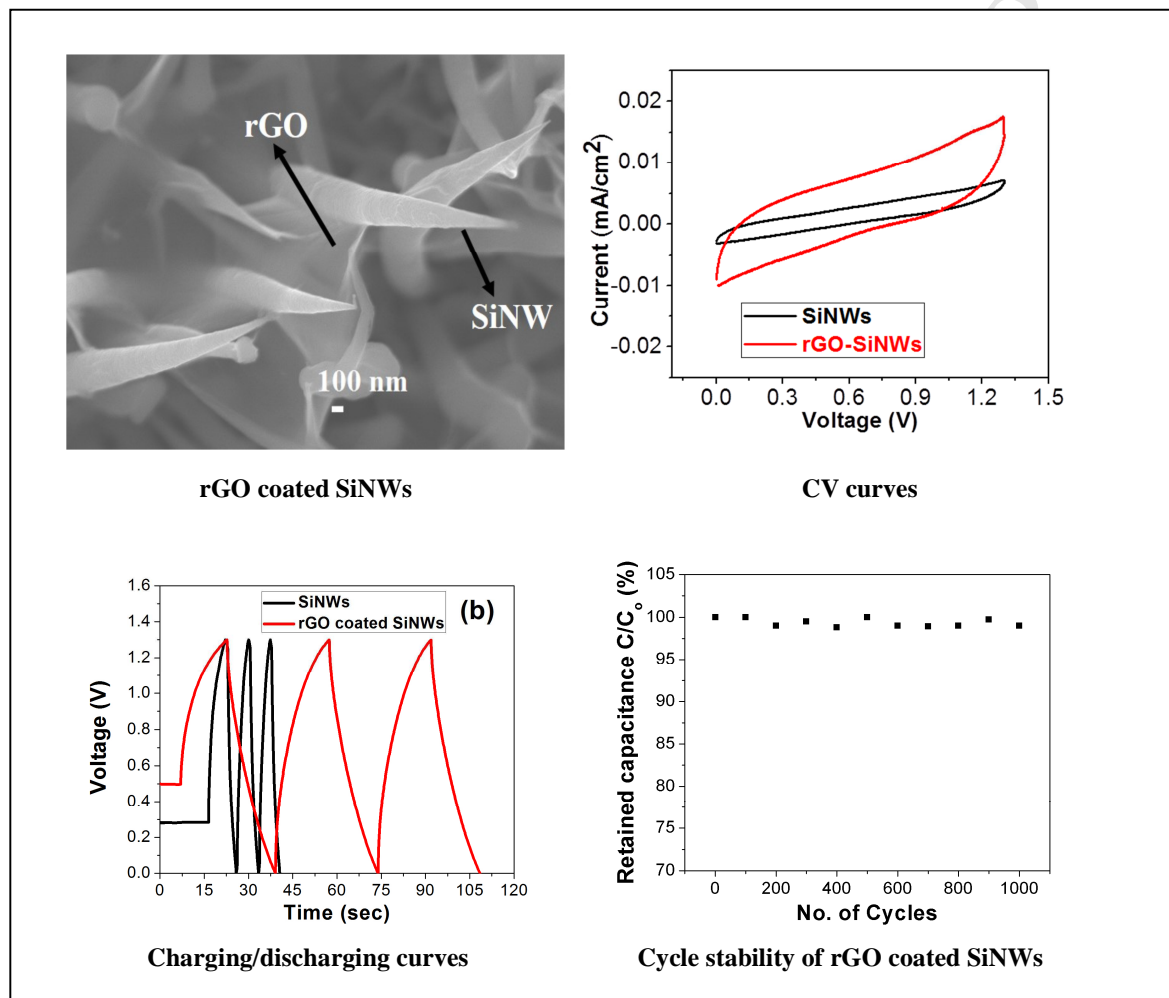
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## Graphical Abstract

This work provides a simple and scalable approach without involving any complex or high temperature steps to improve the specific capacitance of SiNWs which is very important to fabricate on chip SiNWs micro-supercapacitor.



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