ARTICLE IN PRESS



Contents lists available at ScienceDirect

Journal of Crystal Growth



journal homepage: www.elsevier.com/locate/jcrysgro

On the potential and limits of large area seeding for photovoltaic silicon

Nathan Stoddard ^{a,*}, Bianca Gründig-Wendrock ^b, Andreas Krause ^b, Daniel Oriwol ^b, Mariana Bertoni ^c, Tine Uberg Naerland ^c, Ian Witting ^d, Lamine Sylla ^b

^a SolarWorld Americas, Hillsboro, OR, USA

^b SolarWorld Innovations, Freiberg, Germany

^c Dept. of Electrical, Computer and Energy Engineering, Arizona State University, Tempe, AZ, USA

^d Dept. of Materials Science and Engineering, Northwestern University, Chicago, IL, USA

ARTICLE INFO

Article history: Received 22 March 2016 Received in revised form 22 April 2016 Accepted 27 April 2016

Keywords:

A1. Directional solidification

B2. Semiconducting silicon

B3. Solar cells

A2. Seed crystals

ABSTRACT

Single crystal production of silicon for solar cell substrates has relied on the Dash neck technique developed more than 50 years ago. The technique is simple and repeatable and enables truly dislocation free crystal growth. It does have drawbacks, however, including limits on throughput and some structural difficulties. It has long been assumed that dislocation-free growth is not possible by any other method. In the 'quasi-mono' crystal growth technique, one of the key elements is the use of large area single crystal seeds. By melting the seeds at near-equilibrium conditions, it is feasible to avoid the production of dislocations during melting. We will review the dislocation relevant details of the large area seeding process and present best case results for dislocation density, including measured minority carrier lifetimes in excess of 1 ms on p-type material. We will focus on dislocation density exclusive of seed boundaries, but we will also present a potential best-case limit for the technique.

© 2016 Elsevier B.V. All rights reserved.

1. Introduction

Single crystals of silicon, germanium and other materials have been grown in dislocation-free boules for decades using the Czochralski (Cz) and Float Zone (FZ) techniques. The Dash Neck process of pulling a long thin section and then expanding the crystal to a body dimension has been a remarkably successful process [1]. Dislocation-free crystals are valued for their high uniformity, e.g. for semiconductor applications where every square millimeter counts. They also possess low residual stress, even in high pull rate situations, as well as high tensile strength and generally high minority carrier lifetime [2]. The need for a Dash Neck in the crystal growth process is based on the introduction of dislocations from 'thermal shock' at the point when the solid seed is dipped into the melt. This shock is due to the extremely poor pre-heating of the seed in both the Cz and FZ methods. Given the lack of a dedicated heater for the pre-heating of the seed, the seed is primarily heated by radiation from the liquid silicon. Because of the

E-mail address: nathan.stoddard@solarworld.com (N. Stoddard).

http://dx.doi.org/10.1016/j.jcrysgro.2016.04.056 0022-0248/© 2016 Elsevier B.V. All rights reserved. large mismatch in emissivity between solid silicon (\sim 0.70) [3] and liquid silicon (\sim 0.25) [4], a seed placed near a melt will reach radiative equilibrium at a maximum temperature of

 $T_{\rm s} = T_{\rm l} * (\epsilon_{\rm l}/\epsilon_{\rm s})^{1/4} = 1737 \; {\rm K} * (0.25/0.7)^{1/4} = 1343 \; {\rm K}$

giving a ΔT of 394 K. The situation is quite different in large area seeding methods, however. Generally, the seed crystal in large area seeding is heated to a temperature very near the melting temperature at quasi-equilibrium thermal conditions before it is contacted with liquid. As a result, thermal shock is not a given assuming low temperature gradients within the large seed and it is at least theoretically possible to achieve dislocation-free seeding without a necking process. Furthermore, the conditions demanding dislocation-free crystals in semiconductor device applications are not present in photovoltaic (PV) applications, where up to $\sim 10^4$ dislocations/cm² can still deliver high minority carrier lifetime [5]. For this reason, the subject of large-area seeding, as practiced in the Heat Exchanger Method (HEM), Quasi-mono method (QM), and Kyropoulos method, has been of high interest in silicon photovoltaics [6-8]. In this paper, we will review the sources of dislocations in different large area seeding techniques and present new results of an idealized large-area seeding

^{*} Correspondence to: SolarWorld Americas, 25300 NW Evergreen Road, Hillsboro, OR 97124, USA.

experiment pointing to the possibilities of uniform, high performance PV substrates.

2. Methods

Silicon ingots have been grown in directional solidification furnaces. Feedstock silicon is placed on top of seed crystals and then melted, together with some of the seed crystal. Heat is then extracted through the seed to solidify the crystal. Both typical slipcast crucibles and high purity quartz have been used as contact materials for the liquid silicon. Dislocations and defects have been quantified by selective etching and scanning microscopy, while minority carrier lifetime has been measured using a Sinton lifetime tool in Transient mode, both for bulk samples and on passivated wafers. White-beam x-ray topography has been performed using a synchrotron radiation source.

3. Results

A number of process-induced defects threaten the quality of the seed material and the ingot during large-area seeded crystal growth. First, the seed is typically placed under a significant mass of material in the HEM and QM methods. Local stresses can exceed the threshold to produce dislocations in several ways, see Fig. 1. First, the load may be unevenly supported on the seeds, with individual support points from feedstock contact causing high local stress. Second, the seed may be supported unevenly, either due to small chips, coating or dust particles under the seed, or due to gross undulations in the seed support surface. In either case, the effect is large stresses on the seeds. With larger seed crystals, cracking of the seed material is even possible. Any dislocations generated during the heat-up and melt-in process will then propagate into the grown crystal.

A second major issue specific to the QM growth method is the misalignment of seeds. Even seeds cut with high precision rectilinearity from the same seed crystal can experience misalignment in two ways, see Fig. 2. There may be an in-plane rotation between seeds, ϕ , caused by poor packing or a particle between the seeds, and there may be out of plane deflection, θ . Out of plane deflections can arise from variations in the crucible bottom, from seeds placed on the corner radius at the edges, or from small particles underneath them. In any case, a few tenths of a degree misalignment is sufficient to start a dislocation cascade and sub-grain boundary formation.

Another factor affecting seed alignment is an effect where liquid can seep down the sides of the crucible and infiltrate under very small gaps between the seed and the crucible. When the liquid freezes in the cavity, it expands and creates high stress levels, extended defects and seed misorientation.

The effects of some of these process defects can be seen in Fig. 3 and have been reported in [9] for lab-scale ingots A sample has been cut from the bottom of the crystal at the joint of two seed



Fig. 1. Large area seed placed on top of an uneven crucible with feedstock point loads applying locally super-critical pressures.



Fig. 2. Seed plates cut from the same block of seed material can still end up with angular deviations due to poor placement or small chips or variations in the supporting surface.

plates. There was originally a small physical gap between the two seed plates, but most of it has been filled by liquid that flowed down into it and crystallized. The only remaining physical gap is at point (A). A small pinning point (B) between the seed plates has caused an intense local dislocation field. Apart from this, a cellular dislocation structure can be seen throughout the seed, but is especially well defined at (D). The pinning point is also a possible cause for the seed misalignment that is evidenced by the white line gap (C), which is not due to physical space between the seeds (as at the very bottom), but instead from different x-ray deflection angles from the two seed crystals. Above the initial solid/liquid interface, the grown crystal quickly devolves into a tangle of dislocations (F). Finally, silicon nitride particles coming from the release coating sprayed on the inner surface of the crucible have settled to the bottom of the melt and become trapped at the transition from melting to crystal growth. These particles are small and do not necessarily cause dislocation tangles like the seed boundaries do.

There are two other transitions worth discussing at the seed/ grown crystal interface. First, the seed retains the high oxygen concentrations of its Cz origins $(8-12 \times 10^{17} \text{ atoms/cm}^3)$, but the grown crystal has significantly lower oxygen concentration. Second, it is typically observed in OM ingots that the bottom of the ingot has a larger low-lifetime 'red zone' at the bottom of the ingot than normal multicrystalline ingots [10]. In order to understand this, we conducted an experiment where we use a very thick seed (>8 cm), large enough that we can maintain high lifetime material in the center despite the in-diffusion of impurities from the crucible. We melted slightly into the seed with the silicon melt above and then started growth. Deep Level Transient Spectroscopy was used to measure iron within 3 cm on either side of the seed/ growth interface. This data was then compared with a one dimensional model of a plane source of impurities allowed to diffuse during a time and temperature profile typical for that part of the ingot in the crystal growth, annealing and cooling phases. The result gives a good match, as shown in Fig. 4. Note that the upper plot is in mm while the lower is in cm. The FWHM is slightly larger in the measured case, indicating that there may be some enhanced diffusion in the actual process due to structural defects, or that the source layer has a measurable thickness of a few millimeters. The explanation for the iron rich layer could be one of two things. First, it may be that a thin layer of iron-rich liquid is frozen first, before the normal segregating behavior sets up for the impurities. This would involve a non-equilibrium solidification occurring at the very low growth rates of the beginning of growth. A second possibility is that the hottest solid (next to the interface) will have the highest solubility for impurities, and may form a source for the impurity layer, but the temperature gradient in the solid would not account for the sharpness of the source.

Many of the drawbacks and limitations identified so far are the results of very practical limitations inherent to production operations: the flatness of the seed support, the precision of seed placement, the impurity levels present in industrial-scale crucibles and coatings, and the stress concentration from heavy feedstock loads. Several of these trace their origin to a process that was developed to leverage furnaces and crucibles developed for the

Please cite this article as: N. Stoddard, et al., Journal of Crystal Growth (2016), http://dx.doi.org/10.1016/j.jcrysgro.2016.04.056

Download English Version:

https://daneshyari.com/en/article/5489933

Download Persian Version:

https://daneshyari.com/article/5489933

Daneshyari.com