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Test of Ultra Fast Silicon Detectors for Picosecond Time Measurements with a New Multipurpose Read-Out Board

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10 Abstract

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Ultra Fast Silicon Detectors (UFSD) are sensors optimized for timing measurements employing a thin multiplication layer to increase the output signal. A multipurpose read-out board hosting a low-cost, low-power fast amplifier was designed at the University of Kansas and tested at the European Organization for Nuclear Research (CERN) using a 180 GeV pion beam. The amplifier has been designed to read out a wide range of detectors and it was optimized in this test for the UFSD output signal. In this paper we report the results of the experimental tests using 50 μ m thick UFSD with a sensitive area of 1.4 mm². A timing precision below 30 ps was achieved.

- ¹¹ Keywords: Time-of-flight, Time precision, Ultra Fast Silicon Detectors,
- ¹² Charge Sensitive Amplifier, Picosecond Time Measurement

13 1. Introduction

The increasing demand for better timing and spatial resolution has been the key purpose for developing new type of sensors. Low Gain Avalanche Detector (LGAD) technology is one of the most promising advancements in silicon detector technology as it allows a sensible increase of the detector output signal, while keeping all of the known advantages of a silicon substrate, such as low cost and large scale production capabilities. A sensor with an enhanced output

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