



Nature of electrical transport properties of nanocrystalline ZnIn₂Se₄ thin films



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ABSTRACT

ZnIn₂Se₄ thin films were deposited on glass substrates by thermal evaporation technique. Some of ZnIn₂Se₄ films were annealed under vacuum at 623 K for 2 h. Atomic force microscope (AFM) images were analyzed for as-deposited and annealed films. The roughness degree of the film surface decreased under the influence of annealing. DC Electrical conductivity studied as a function of temperature. Two activation energies were determined that $\Delta E_1 = 0.44$ eV and $\Delta E_2 = 0.65$ eV. Using thermo-electric measurements, the thermoelectric power factor (P), carrier concentration (n) and mobility (μ) were calculated. Current density–voltage characteristics of Al/ZnIn₂Se₄/Al sandwich structure were examined. Different mechanisms were obtained; ohmic conduction mechanism at lower voltages and space charge limited conductivity (SCLC) mechanisms at higher voltages.

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1. Introduction

Zinc Indium Selenide (ZnIn₂Se₄) is a ternary chalcogenide semiconductor of type A^{II} B₂^{III}X₄^{VI}, where A=Zn, Cd or Hg, B=Al, In or Ga, and X=Se, S or Te [1,2]. Ternary semiconducting compounds have been generally researched as a result of their potential applications in the electro-optic, optoelectronic, and nonlinear optical devices. [3]. These compounds are majority crystallized in a tetragonal structure [4]. Consequently, there is much interest in the preparation and characterization of chalcogenide thin films [5]. Thin films of ZnIn₂Se₄ have been grown using different techniques such as spray pyrolysis technique [5], flash evaporation technique [4], chemical bath deposition [6,7] and thermal evaporation [8]. ZnIn₂Se₄ thin films have wide applications in solar cells and optoelectronic devices [5], as a memory switching device [9] and in photo-detector device [10]. Likewise, it utilized as the novel buffer layer in place of toxic CdS and demonstrates a cell efficiency of 15.3% in the fabrication of Cu(In,Ga)Se₂ based-solar cells [7,11]. The interface between ZnIn₂Se₄ and Cu(In,Ga)Se₂ produces a depletion region which facilitates the formation of the p–n junction. However, the incorporation of Zn ions into Cu(In,Ga)Se₂ films is difficult to control, and the excess dopants cause the formation of the impurities [12]. Some studies analyzed the dopant concentration in heterostructure and found that increasing the difference between diffusion coefficients of layers of heterostructure leads to increase the homogeneity of impurities in doped region [13].

The physical properties that underpin the operation of electronic devices depend on chemical bonding and crystalline nature of the materials [14]. There are many characterization's tools that can provide a useful insight into the mechanisms of the charge transport through films of materials. Analyses of electron transport parameters are promising methods based on the measurements of device I – V characteristics, I – V characteristic nonlinearity [15] and electronic noise characteristics [16–20]. In the present work, thin films of ZnIn₂Se₄ were prepared by thermal evaporation method and deposited onto glass substrates. A detailed experimental study carried out on the structural and electrical transport properties of ZnIn₂Se₄ films as a function of temperature: in particular, Atomic force microscope, electrical conductivity, thermoelectric power and J – V characteristics.

2. Experimental technique

The ingots of Zinc Indium Selenide were prepared by fusion of stoichiometric quantities of pure elements in vacuum sealed silica tubes. They were left at 1323 K for 10 h and then cooled to room temperature over 48 h. Thin films with different thicknesses were prepared by the thermal evaporation technique under a vacuum of about 10^{-4} Pa using coating unit (Edwards type E306A, England). The deposition rate was controlled at 2.5 nm s⁻¹. The film thickness was calculated using a quartz crystal thickness monitor (FTM4, Edwards) and then it was checked by the Tolansky's interferometric technique [21]. The film depositions were made at room temperature. Some films were annealed under vacuum at 623 K for 2 h. The 2D and 3D images of the morphology of film sur-

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face were examined utilizing atomic force microscopy technique, model (Wet-SPM Shimadzu). The particles mean radius and surface roughness of ZnIn_2Se_4 thin films were calculated by using computer programming. The DC electrical measurements were carried out using two probe method for a planar sample. The ohmic electrodes at the two ends of the thin films were made by thermal evaporation of Al. The electrical resistivity of films was measured by a high internal impedance electrometer (Keithley 617A). Then, the DC conductivity (σ_{DC}) was calculated from the well-known relation $\sigma_{\text{DC}} = L/RA$, where L is the length of the film, R is electrical resistance and A is the cross section area of the sample. For the thermoelectric power measurements, Al electrodes were deposited into the two ends of the planer ZnIn_2Se_4 films. The thermoelectric power (s) was detected by measuring the voltage for a temperature gradient of 10 K over the samples using a programmable electrometer. The current density–voltage (J – V) characteristics for a sandwich sample, that has Al/ ZnIn_2Se_4 /Al structure, were measured utilizing the high impedance electrometer (Keithley 617A) as a current source and ammeter. The sample temperature measured using a chromel–alumel thermocouple appended to a digital thermometer.

3. Results and discussion

3.1. Structural topography

In our previous paper [8], we studied the structural properties of ZnIn_2Se_4 thin films using X-ray diffraction technique. The deposited films at room temperature showed nanocrystalline nature with crystallite size of about a few nanometers.

The knowledge of the surface topography can be obtained using atomic force microscopy (AFM) technique, which gives an excellent tool to study morphology and texture of diverse surfaces [22]. In the present work, the surface morphology for the as-deposited film of ZnIn_2Se_4 with thickness 473 nm and the annealed one under vacuum at 623 K for 2 h were analyzed using atomic force microscope (AFM) technique. Fig. 1(a, b) shows the 2D and 3D images of AFM for as-deposited and annealed films, respectively. The average roughness is found to decrease from 1.41 nm for the as-deposited ZnIn_2Se_4 film to 1.20 nm for the annealed ZnIn_2Se_4 film. Thus, under the influence of annealing the film provides a homogenous nature. The mean radius of particles in ZnIn_2Se_4 film decreased from 70 nm in the as-deposited film to 20 nm for the annealed one as observed in Fig. 1.

3.2. Electrical conductivity

3.2.1. DC electrical conductivity

Fig. 2 shows the temperature dependence of DC electrical conductivity (σ_{DC}) of ZnIn_2Se_4 films with thickness 371 nm. It is clear from that σ_{DC} increases with increasing temperature. Thus, DC conductivity obeys the Arrhenius relation indicating a semiconducting transport behavior [23]. The curve of σ_{DC} reveals two distinct linear regions. This result refers to the existence of two transport mechanisms, which gives two activation energies ΔE_1 and ΔE_2 for the conduction of free charge carriers. The activation energies are calculated using the following relation [24,25]:

$$\sigma_{\text{DC}} = \sigma_0 e^{-\frac{\Delta E}{kT}} \quad (1)$$

where σ_0 is the pre-exponential constant, k is the Boltzmann's constant, T is the absolute temperature and ΔE is the activation energy for a certain region. At lower range of temperature (293–312 K), the value of ΔE_1 is found to be 0.44 eV. ΔE_1 is attributed to the extrinsic conduction mechanism. At higher range of temperature (~312–370 K), the value of ΔE_2 is found to be 0.65 eV. ΔE_2 is ascribed to the intrinsic conduction mechanism. The value of ΔE_2

is approximately about half the onset energy gap obtained from optical measurements [8].

3.2.2. Thermoelectric power measurement

The Seebeck coefficient, S , for ZnIn_2Se_4 film with thickness 371 nm is measured as a function of temperature from 303 K to 350 K. It determined using the following relation [26]:

$$S = \frac{\Delta V}{\Delta T} \quad (2)$$

where ΔV is the thermo-emf produced across the sample due to the temperature difference ΔT . The variation of Seebeck coefficient as a function of temperature is shown in Fig. 3. From the figure, Seebeck coefficient is observed to be negative affirming to the n-type polarity for ZnIn_2Se_4 film. Thus, the most predominant carriers are electrons. Also, Seebeck coefficient increases with increasing temperature in the investigated range of temperature. In the extrinsic region ($T < 312$ K), a sharp increase in Seebeck coefficient is owing to the increasing number of thermally excited carriers [27]. Fig. 4 demonstrates the variation for both of S and $\ln \sigma_{\text{DC}}$ against the reciprocal of temperature in the intrinsic region of conduction ($T > 312$ K). In this region, the moderate increase in Seebeck coefficient may be due to the generation of the electrons from the valence band to the conduction band [28].

For assessing the potential of a thermoelectric material, it is often to use the thermoelectric power factor (P). It is evaluated from the values of DC electrical conductivity and Seebeck coefficient as [29–31]:

$$P = S^2 \sigma_{\text{DC}} \quad (3)$$

The P value is about $0.14 \mu\text{W}/\text{mK}^2$ at 305 K. Also, the thermoelectric power measurement is used to evaluate the carrier mobility (μ) and carrier concentration. The free carrier concentration, n , is determined as a function of temperature according the following equation [32,33]:

$$n = 2 \left(\frac{2\pi m^* kT}{h^2} \right)^{3/2} e^{(2k-qS)/k} \quad (4)$$

where h is a Planck's constant, m^* is the effective mass that is taken as $0.15m_0$ [34], q is the electronic charge and k is in eV/K. Using the values of S , the value of n is calculated. The value of n increases with increasing temperature in the range of 1.1 – $1.4 \times 10^{25} \text{ m}^{-3}$ which corresponding to the temperature range of 300–360 K.

The mobility (μ) of the charge carriers is determined from the relations [25,35]:

$$\mu = \frac{\sigma_{\text{DC}}}{nq}, \quad \mu = \mu_0 e^{\left(\frac{-\Delta E_\mu}{kT}\right)} \quad (5)$$

where μ_0 is the grain boundary limited mobility and ΔE_μ is the mobility activation energy. Fig. 5 represents the temperature dependence of the charge carriers mobility. As observed μ values increase with increasing temperature. The activation energy for the mobility is determined by plotting $\ln \mu$ against $1000/T$ as seen in the inset of Fig. 5. The calculated values of μ_0 and ΔE_μ are $2.91 \times 10^{-3} \text{ m}^2 \text{ V}^{-1} \text{ S}^{-1}$ and 0.49 eV, respectively.

3.2.3. Current density–voltage (J – V) characteristics

J – V characteristics are taken at constant temperature (293, 303, 308, 313 K) for Al/ ZnIn_2Se_4 /Al sandwich structure of a sample deposited on glass substrate. The configuration of the sample design is shown in Fig. 6. Fig. 7 represents a typical $\log J$ – $\log V$ characteristics for Al/ ZnIn_2Se_4 /Al at different temperatures. The current density increases with the increase in the applied voltage and temperature. The graphs for J – V characteristics reveal three distinct regions with different slopes (m) for each temperature, where $J \propto V^m$.

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