ELSEVIER

Contents lists available at ScienceDirect

#### **Fusion Engineering and Design**

journal homepage: www.elsevier.com/locate/fusengdes



## Design of the power system for dynamic resonant magnetic perturbation coils on the J-TEXT tokamak

B. Yi, Y.H. Ding\*, M. Zhang, B. Rao, J.Y. Nan, W.B. Zeng, M.Y. Zheng, H.Y. Xu, G. Zhuang, Y. Pan

State Key Laboratory of Advanced Electromagnetic Engineering and Technology, Huazhong University of Science and Technology, Wuhan 430074, China

#### HIGHLIGHTS

- ▶ We introduce the dynamic resonant magnetic perturbation coils system on J-TEXT.
- ▶ Details of the design of the power supply system have been presented.
- ► At DC mode, two antiparallel 6-pulse phase thyristor rectifiers were chosen.
- ► An AC-DC-AC converter including a series resonant inverter was adopted for AC mode.
- ► Some engineering testing result was given in this paper.

#### ARTICLE INFO

# Article history: Received 14 September 2012 Received in revised form 11 February 2013 Accepted 13 February 2013 Available online 9 March 2013

Keywords: In-vessel saddle coils Resonant magnetic perturbation (RMP) Power supply J-TEXT

#### ABSTRACT

A set of in-vessel saddle coils has been installed on J-TEXT tokamak. They are proposed for further researches on controlling tearing modes and driving plasma rotation by static and dynamic resonant magnetic perturbations (RMPs). The saddle coils will be energized by DC with the amplitude up to 10 kA, or AC with maximum amplitude up to 5 kA within the frequency range of 1–5 kHz. At DC mode two antiparallel 6-pulse phase thyristor rectifiers are chosen to obtain bidirectional current, while at AC mode an AC-DC-AC converter including a series resonant inverter can generate current of various amplitudes and frequencies. The paper presents the design of the power supply system, based on the definition of the power supply requirements and the feasibility of implementation of the topology and control strategy. Some simulation and experimental results are given in the end.

© 2013 Elsevier B.V. All rights reserved.

#### 1. Introduction

The control of plasma instabilities is a very important issue in Tokamak experiments. Among today's solutions, active control with in-vessel saddle coils is regarded as one of the most effective approaches to study the tearing modes, error field penetration, mitigation of edge localized modes and so on [1,2].

A set of active coils has already been mounted on the outside of the vacuum vessel of J-TEXT [3], which can generate static magnetic perturbation, only. In order to enhance experiments on the interaction between applied RMPs and plasma, it is planned to install groups of in-vessel saddle coils. This upgrade scheme consists of the design, fabrication and installation of the coils and the relevant power supply (PS) system.

E-mail address: yhding@mail.hust.edu.cn (Y.H. Ding).

Based on considerations on the vessel geometry and electromagnetic features of the coils, four groups of saddle coils are designed and distributed symmetrically on the inside of the J-TEXT vessel [4]. The layout of the dynamic resonant magnetic perturbation (DRMP) coils is shown in Fig. 1. The coil sets are divided into two parts consisting of two groups of coils which are spatially 180° phase shifted to each other in toroidal direction (Fig. 2). Each part will be energized independently by DC or AC, using a real-time control system. In this paper, the requirements for the DRMP coils system are introduced first. Afterwards, a detailed description of the PS system design and some testing results are presented.

#### 2. PS requirements and design

#### 2.1. PS requirements

In literature [4], the magnetic field design of the DRMP coils system has been described deeply. In order to generate strong enough m/n = 2/1 (m, n: poloidal and toroidal mode numbers respectively) perturbation field in both DC and AC operation mode, the PS should have the electrical features shown in Table 1. The impedance of

<sup>\*</sup> Corresponding author at: Room 217, Building East 15, East campus of Huazhong University of Science and Technology, 1037 Luoyu Road, Wuhan 430074, China. Tel.: +86 02787543004x8217; fax: +86 02787793060.

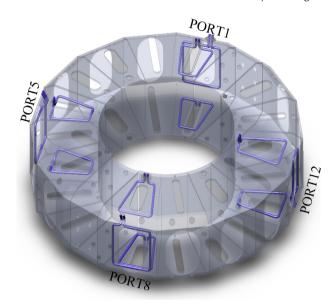


Fig. 1. Layout of the DRMP coils on J-TEXT.

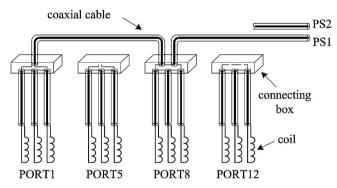


Fig. 2. Series connection of the coils.

the coil load has been calculated earlier, and it is in agreement with the results of the measurements reported in Table 2. To meet all requirements with suitable design margin, the output power should be 2500 kW and 1200 kW for the DC and AC PS respectively.

 Table 1

 Electrical requirements of the DRMP coils on J-TEXT.

	DC power	AC power
Number of units	2	2
Frequency	0	1-5 kHz
Output current	$\pm 10\mathrm{kA}$	5 kA(peak)@5 kHz
Output voltage	±250 V	<1000 V(peak)
Ripple of current	<5%	-
THD of current	_	<5%
Duty cycle	1 s/300 s	1 s/300 s

**Table 2** Parameters of the coils.<sup>a</sup>

Frequency	Port1+Port8	Port5+Port12
0	$4.6\mathrm{m}\Omega$	$4.4\mathrm{m}\Omega$
1000 Hz	15.91 m $\Omega$ /6.86 μH	$15.94  \text{m}\Omega/6.77  \mu\text{H}$
2000 Hz	$22.54 \mathrm{m}\Omega/6.38\mu\mathrm{H}$	$22.17  m\Omega/6.31  \mu H$
3000 Hz	$28.63  \text{m}\Omega/6.12  \mu\text{H}$	$27.94\text{m}\Omega/6.04\mu\text{H}$
4000 Hz	$34.74\mathrm{m}\Omega/5.91\mu\mathrm{H}$	$34.19\text{m}\Omega/5.87\mu\text{H}$
5000 Hz	$40.47{\rm m}\Omega/5.78{\rm \mu H}$	$40.01\text{m}\Omega/5.74\mu\text{H}$

<sup>&</sup>lt;sup>a</sup> Measure results using Precision Magnetics Analyzer 3260B (Wayne Kerr electronics).

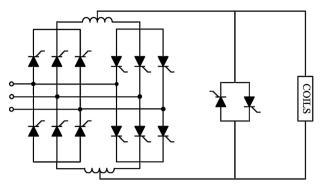


Fig. 3. Selected topology for DC PS.

This kind of PS features fairly unique electrical parameters and characteristics, for example, fast dynamic response, high quality of output waveform and wide tuning range. Even so, many Tokamak experiment devices have completed the design of PS for active coils based on their own requirements [2]. Any of these designs is a result of comprehensive consideration of the feasibility, difficulty, cost, size, etc. For J-TEXT, DC and AC PS system for the DRMP coils are designed separately. So, two DC PSs and two AC PSs are needed for the coils system.

#### 2.2. DC PS design

The DC PS should provide controllable DC with a maximum value of 10 kA. A phase-controlled thyristor rectifier could be the best choice, which is a cost-effective and highly developed technology. A detailed comparison of 12-pulse with 6-pulse rectifier has been done before adoption of 6-pulse rectifier topology, in terms of ripple, cost and size. In order to meet requirements of different operation scenarios, the DC PS must have the capacity for generating various current waveforms. The waveforms could be unipolar or bipolar, symmetrical or asymmetrical, which is quite helpful for plenty experiments. As shown in Fig. 3, two antiparallel 6-pulse phase-controlled thyristor rectifiers have been chosen.

Safety and reliability are both very important for the PS system. A bidirectional bypass branch that can offer a path for the load current when a fault occurs is added to the output of the rectifier. The branch is comprised of several thyristors that are in parallel. The smoothing reactor with three terminals combines the two antiparallel three-phase bridge rectifiers, which guarantees that the PS can be operated safely in four-quadrant operation mode. Moreover, the reactor also plays the role of a filter, making the output current wave smoother.

#### 2.3. AC PS design

A broad variety of topologies has been used in the design of AC PS that can supply thousands of amperes current with bandwidth up to thousands of hertz in the research of plasma instabilities control [2,5]. The difficulty in finding an appropriate technical solution is to choose the DC/AC converter implemented by fully controllable semiconductor switches. For semiconductors, the maximum of switching frequency decreases when rating power increases. The best choice for the semiconductor switch is the insulated gate bipolar transistor (IGBT) in the given range of power and frequency defined in Table 1.

The next step is to select an appropriate topology that can not only satisfy the output requirements, but also reduce the switching frequency as much as possible. Among the possible considerations, cascaded H-bridges multilevel inverter, using carrier-phase-shifted

#### Download English Version:

### https://daneshyari.com/en/article/6746367

Download Persian Version:

https://daneshyari.com/article/6746367

**Daneshyari.com**