



Observer backstepping control of DFIG-Generators for wind turbines variable-speed: FPGA-based implementation



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ABSTRACT

In this paper, we present a new contribution for the control of Wind-turbine energy systems, a nonlinear robust control of active and reactive power by the use of the Adaptive Backstepping approach based in double-fed asynchronous generator (DFIG-Generator).

Initially, a control strategy of the MPPT for extraction of maximum power of the turbine generator is presented. Thereafter, a new control technique for wind systems is presented. This control system is based on an adaptive pole placement control approach integrated to a Backstepping control system. The stability of the system is shown using Lyapunov technique. Using the FPGA to implement the order gives us a better rapidity. A Benchmark was realized by a prototyping platform based on DFIG-generator, FPGA and wind-turbine; the experimental results obtained show the effectiveness and the benefit of our contribution.

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1. Introduction

Today, wind energy has become a viable solution for the production of energy, in addition to other renewable energy sources. While the majority of wind turbines are fixed speed, the number of variable speed wind turbines is increasing [1]. The *Doubly-Fed Asynchronous Generator (DFIG)* with Backstepping control is a machine that has excellent performance and is commonly used in the wind turbine industry [2,3]. There are many reasons for using the Doubly-Fed Asynchronous Generator (DFIG) for wind turbine a variable speed, such as reducing efforts on mechanical parts, noise reduction and the possibility of control of active power and reactive.

The wind system using DFIG generator and a “back-to-back” converter that connects the rotor of the generator and the network has many advantages. One advantage of this structure is that the power converters used are dimensioned to pass a fraction of the

total system power [5,6]. This allows reducing losses in the power electronics components. The performances and power generation depends not only on the DFIG generator, but also the manner in which the two parts of “back-to-back” converter are controlled.

The power converter machine side is called “Rotor Side Converter” (RSC) and the converter Grid-side power is called “Grid Side Converter” (GSC). The RSC converter controls the active power and reactive power produced by the machine. As the GSC converter, it controls the DC bus voltage and power factor network side.

The speed performance of new components and the flexibility inherent of all programmable solutions give today many opportunities in the field of digital implementation for control systems. This is true for software solutions as microprocessor or DSP (Digital Signal Processor). However, specific programmable hardware technology such as Field Programmable Gate Array (FPGA) can also be considered as an especially appropriate solution in order to boost performances of controllers [1–3]. Indeed, these generic components combine low cost development, thanks to their re-configurability, use of convenient software tools and more and more significant integration density [4,5].

The FPGA technology is now used by an increasing number of designers in various fields of application such as signal processing [6], telecommunication, video, embedded control systems, and

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