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$O(t^{-\alpha})$ -synchronization and Mittag-Leffler synchronization for the fractional-order memristive neural networks with delays and discontinuous neuron activations

Jiejie Chen ^{[a](#page-0-0)[,c](#page-0-1)}, Boshan Chen ^{[b](#page-0-2)}, Zhigang Zeng ^{[c,](#page-0-1)[d,](#page-0-3)}[*](#page-0-4)

^a *College of Computer Science and Technology, Hubei Normal University, Huangshi 435002, Hubei, China*

^b *College of Mathematics and Statistics, Hubei Normal University, Huangshi 435002, Hubei, China*

c *School of Automation, Huazhong University of Science and Technology, Wuhan 430074, China*

^d *Key Laboratory of Image Processing and Intelligent Control of Education Ministry of China, Wuhan 430074, China*

a r t i c l e i n f o

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A B S T R A C T

This paper investigates *O*(*t* −α)-synchronization and adaptive Mittag-Leffler synchronization for the fractional-order memristive neural networks with delays and discontinuous neuron activations. Firstly, based on the framework of Filippov solution and differential inclusion theory, using a Razumikhin-type method, some sufficient conditions ensuring the global $O(t^{-\alpha})$ -synchronization of considered networks are established via a linear-type discontinuous control. Next, a new fractional differential inequality is established and two new discontinuous adaptive controller is designed to achieve Mittag-Leffler synchronization between the drive system and the response systems using this inequality. Finally, two numerical simulations are given to show the effectiveness of the theoretical results. Our approach and theoretical results have a leading significance in the design of synchronized fractional-order memristive neural networks circuits involving discontinuous activations and time-varying delays.

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1. Introduction

The fractional calculus has become a great research topic in recent years due to its many applications in the field of physics and engineering [\(Kilbas,](#page--1-0) [Srivastava,](#page--1-0) [&](#page--1-0) [Trujillo,](#page--1-0) [2006;](#page--1-0) [Podlubny,](#page--1-1) [1999\)](#page--1-1). In fact, many of the real world objects are generally identified and described by the fractional-order model. This model is more accurate than the integer-order model. The main advantage of fractional-order model in comparison with integer-order model is that a fractional derivative provides an excellent tool in the description of memory and hereditary properties of various processes. In addition, the fractional-order model has more degrees of freedom and unlimited memory (infinite memory). Based on these features, some researchers have introduced fractional calculus in neural network models to form a fractional-order neural network model. Therefore, it is needed to study the dynamics of fractionalorder neural networks. For the past few years, the analysis of fractional-order neural networks has become an increasing interest and growing area of research, and the dynamical behaviors of

E-mail addresses: chenjiejie118@gmail.com (J. Chen), chenbs@hbnu.edu.cn (B. Chen), zgzeng@hust.edu.cn (Z. Zeng).

fractional-order neural networks, such as synchronization, stability, and state estimation have been discussed in Refs. [Chen](#page--1-2) [and](#page--1-2) [Chen](#page--1-2) [\(2015a,](#page--1-2) [2015b,](#page--1-2) [2016\)](#page--1-2), [Qi,](#page--1-3) [Li,](#page--1-3) [and](#page--1-3) [Huang](#page--1-3) [\(2014\)](#page--1-3), [Rakkiyappan,](#page--1-4) [Velmurugan,](#page--1-4) [and](#page--1-4) [Cao](#page--1-4) [\(2015\)](#page--1-4), [Rakkiyappan,](#page--1-5) [Velmurugan,](#page--1-5) [Rihan,](#page--1-5) [and](#page--1-5) [Lakshmanan](#page--1-5) [\(2016\)](#page--1-5), [Yan,](#page--1-6) [Cao,](#page--1-6) [and](#page--1-6) [Liang](#page--1-6) [\(2016\)](#page--1-6) and [Yang](#page--1-7) [and](#page--1-7) [DWC](#page--1-7) [\(2016\)](#page--1-7) and references therein.

Memristor is a contraction of memory resistor, which is a new nonlinear electric circuit element, that describes the relationship between electric charge and magnetic flux. The memristors were first introduced theoretically by Chua [\(Chua,](#page--1-8) [1971\)](#page--1-8) and it has been realized practically by the research team of HP Lab in 2008 [\(Strukov,](#page--1-9) [Snider,](#page--1-9) [Stewart,](#page--1-9) [&](#page--1-9) [Williams,](#page--1-9) [2008\)](#page--1-9). Memristor is a twoterminal element with variable resistance and its value is not unique, which depends on the magnitude and polarity of the voltage applied to it and the length of the time that the voltage has been applied. When the voltage is turned off, the memristor remembers its most recent value until next time it is turned on. Therefore, memristors have been used for nonvolatile memory storage. Based on the memristors, a new type of neural network model, called the memristor-based neural networks, has been introduced in the literature and dynamical behaviors have been investigated (see [Abdurahman](#page--1-10) [and](#page--1-10) [Jiang](#page--1-10) [\(2016\)](#page--1-10), [Abdurahman,](#page--1-11) [Jiang,](#page--1-11) [and](#page--1-11) [Rahman](#page--1-11) [\(2015\)](#page--1-11), [Chandrasekar,](#page--1-12) [Rakkiyappan,](#page--1-12) [Cao,](#page--1-12) [and](#page--1-12) [Lakshmanan](#page--1-12) [\(2014\)](#page--1-12), [Chen,](#page--1-13) [Zeng,](#page--1-13) [and](#page--1-13) [Jiang](#page--1-13) [\(2014a,](#page--1-13) [b,](#page--1-13) [c\)](#page--1-13), [Hu](#page--1-14) [and](#page--1-14) [Wang](#page--1-14) [\(2010\)](#page--1-14) and

^{*} Corresponding author at: School of Automation, Huazhong University of Science and Technology, Wuhan 430074, China.

[Wang,](#page--1-15) [Li,](#page--1-15) [Peng,](#page--1-15) [Xiao,](#page--1-15) [and](#page--1-15) [Yang](#page--1-15) [\(2014\)](#page--1-15) and references therein). In addition, the analysis of memristor-based neural networks is necessary on account of its potential applications in next generation computer and powerful brain-like neural computer [\(Chua,](#page--1-8) [1971;](#page--1-8) [Wang,](#page--1-16) [Chen,](#page--1-16) [Xi,](#page--1-16) [Li,](#page--1-16) [&](#page--1-16) [Dimitrov,](#page--1-16) [2009\)](#page--1-16).

As is well known, the chaos synchronization has received great attention in the investigation of neural networks since its successful applications in a variety of fields. Therefore, the synchronization phenomenon of neural networks is an important issue that is explored by many researchers (see, e.g., Refs. [Abdurahman](#page--1-10) [and](#page--1-10) [Jiang](#page--1-10) [\(2016\)](#page--1-10), [Abdurahman](#page--1-11) [et](#page--1-11) [al.](#page--1-11) [\(2015\)](#page--1-11), [Chandrasekar](#page--1-12) [et](#page--1-12) [al.](#page--1-12) [\(2014\)](#page--1-12), [Li](#page--1-17) [and](#page--1-17) [Cao](#page--1-17) [\(2015\)](#page--1-17), [Mathiyalagan,](#page--1-18) [Park,](#page--1-18) [and](#page--1-18) [Sakthivel](#page--1-18) [\(2015\)](#page--1-18), [Shen,](#page--1-19) [Wu,](#page--1-19) [and](#page--1-19) [Park](#page--1-19) [\(2015\)](#page--1-19) and [Stamova](#page--1-20) [\(2014\)](#page--1-20)). Also, the synchronization of fractional-order dynamical systems becomes a stimulating and inspiring problem due to its potential applications in ranging from computer science to biology, from physics to engineering, even from economics to brain science, secure communication, and [c](#page--1-22)ontrol processing (see Refs. [Bao,](#page--1-21) [Park,](#page--1-21) [and](#page--1-21) [Cao](#page--1-21) [\(2015,](#page--1-21) [2016\)](#page--1-21), [Vel](#page--1-22)[murugan](#page--1-22) [and](#page--1-22) [Rakkiyappan](#page--1-22) [\(2016\)](#page--1-22) and [Velmurugan,](#page--1-23) [Rakkiyappan,](#page--1-23) [and](#page--1-23) [Cao](#page--1-23) [\(2016\)](#page--1-23)).

Time delays, especially time-varying delays, are unavoidably encountered in the signal transmission among the neurons, which will affect the stability of neural networks and may lead to some complex dynamic behaviors (see [Ahn,](#page--1-24) [Shi,](#page--1-24) [and](#page--1-24) [Wu](#page--1-24) [\(2015\)](#page--1-24), [Saravanakumar,](#page--1-25) [Ali,](#page--1-25) [Ahn,](#page--1-25) [Karimi,](#page--1-25) [and](#page--1-25) [Shi](#page--1-25) [\(2017\)](#page--1-25) and references therein). In [Chen](#page--1-2) [and](#page--1-2) [Chen](#page--1-2) [\(2015a\)](#page--1-2) we have studied global *O*(*t* −α) stability and global asymptotical periodicity for a non-autonomous fractional-order neural networks with time-varying delays by a Razumikhin-type method (see [Chen](#page--1-26) [and](#page--1-26) [Chen](#page--1-26) [\(2015b\)](#page--1-26)). In [Bao](#page--1-21) [et](#page--1-21) [al.](#page--1-21) [\(2015\)](#page--1-21) Bao et al. discussed the adaptive synchronization of fractional-order memristor-based neural networks with time delay by combining the adaptive control, linear delay feedback control, and a fractional-order inequality. The results on exponential synchronization of memristor-based neural networks with delay and discontinuous neuron activations are established via two types of discontinuous controls: linear feedback control and adaptive control in [Abdurahman](#page--1-10) [and](#page--1-10) [Jiang](#page--1-10) [\(2016\)](#page--1-10). However, to the best of our knowledge, there are very few or even no results on the *O*(*t* −α)-synchronization and Mittag-Leffler synchronization of fractional-order memristive neural networks with delay and discontinuous neuron activations. Motivated by the previous works and background, the main purpose of this paper is to fill this gap. The present paper at least have four highlights as follows: (1) Two new types of synchronization, *O*(*t* −α)-synchronization and Mittag-Leffler synchronization, are proposed, which can better describe synchronization feature of fractional-order systems. (2) Some sufficient conditions ensuring the global $O(t^{-\alpha})$ -synchronization of considered networks are established via a linear-type discontinuous control. (3) A new fractional differential inequality is established and two new discontinuous adaptive controllers are designed to achieve Mittag-Leffler synchronization between the drive system and the response systems using this inequality. (4) The works are new that fill some gap of the existing works. (5) Our results generalize and improve those of existing literature.

The rest of the paper is organized as follows. In Section [2,](#page-1-0) the drive–response systems are introduced. In addition, some assumptions and definitions together with some useful lemmas needed in this paper are presented. In Section [3,](#page--1-27) we devote to investigating the $O(t^{-\alpha})$ -synchronization between the drive system and the response systems by designing a linear-type discontinuous controller. In Section [4,](#page--1-28) a new discontinuous feedback controller is designed to achieve Mittag-Leffler synchronization between the drive system and the response systems. In Section [5,](#page--1-29) two numerical examples and their simulations are given to illustrate

the effectiveness of the obtained results. Finally, some general conclusions are drawn in Section [6.](#page--1-30)

2. Preliminaries

In order to describe our model, we will recall some definitions of fractional calculation.

The fractional integral with order α for a function $f(t)$ is defined as

$$
{}_{t_0}^{RL}D_t^{-\alpha}f(t)=\frac{1}{\Gamma(\alpha)}\int_{t_0}^t(t-s)^{\alpha-1}f(s)ds
$$

where $t \ge t_0$ and $\alpha > 0$, $\Gamma(\cdot)$ is the gamma function, that is

$$
\Gamma(\alpha) = \int_0^\infty t^{\alpha-1} e^{-t} dt.
$$

The Riemann–Liouville derivative of fractional with order α of function $f(t)$ is given as

$$
\begin{split} \frac{\kappa}{t_0} D_t^{\alpha} f(t) &= \frac{d^n}{dt^n} D_{t_0,t}^{-(n-\alpha)} f(t) \\ &= \frac{1}{\Gamma(n-\alpha)} \frac{d^n}{dt^n} \int_{t_0}^t \frac{f(s)}{(t-s)^{\alpha-n+1}} ds. \end{split}
$$

The Caputo's fractional derivative with order α for a function $f \in C^{n+1}([t_0, +\infty), R)$ is defined by

$$
D_{t_0}^{\alpha} f(t) = \frac{1}{\Gamma(n-\alpha)} \int_{t_0}^t \frac{f^{(n)}(s)}{(t-s)^{\alpha-n+1}} ds
$$

where $t > t_0$ and *n* is a positive integer such that $n - 1 < \alpha < n$.

In this paper, we consider a class of delayed fractional-order memristive neural network (DFMNN) with discontinuous activation functions described by the following equation:

$$
D_{t_0}^{\alpha}x_i(t) = -c_i(x_i(t))x_i(t) + \sum_{j=1}^n a_{ij}(x_j(t))f_j(x_j(t))
$$

+
$$
\sum_{j=1}^n b_{ij}(x_j(t - \tau_{ij}(t)))g_j(x_j(t - \tau_{ij}(t))) + I_i,
$$
 (2.1)

where $i = 1, \ldots, n, t \ge 0$ 0 < α < 1, *n* corresponds to the number of units in a neural network, *xi*(*t*) denotes the state variable associated with the *i*th neuron, *Iⁱ* denotes the external input, which is a constant, $\tau_{ii}(t)$ is a nonnegative function representing the finite speed of the axonal signal transmission in the time *t*, $f_i(\cdot)$ and $g_i(\cdot)$ are all the nonlinear activation function, that can be discontinuous; $c_i(x_i)$, $a_{ij}(x_j)$ and $b_{ij}(x_j)$ ($i, j = 1, \ldots, n$) are, respectively, the memristor-based connection weights and those associated with time delays, that are given by

$$
a_{ij}(\xi) = \begin{cases} \hat{a}_{ij}, & |\xi| > T_j^a, \\ \text{unsureness}, & |\xi| = T_j^a, \\ \check{a}_{ij}, & |\xi| < T_j^a, \end{cases}
$$
 (2.2)

$$
b_{ij}(\xi) = \begin{cases} \hat{b}_{ij}, & |\xi| > T_j^b, \\ \text{unsureness}, & |\xi| = T_j^b, \\ \check{b}_{ij}, & |\xi| < T_j^b, \end{cases}
$$
 (2.3)

and

$$
c_i(\xi) = \begin{cases} \hat{c}_i, & |\xi| > T_j^c, \\ \text{unsureness}, & |\xi| = T_j^c, \\ \check{c}_i, & |\xi| < T_j^c, \end{cases} \tag{2.4}
$$

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