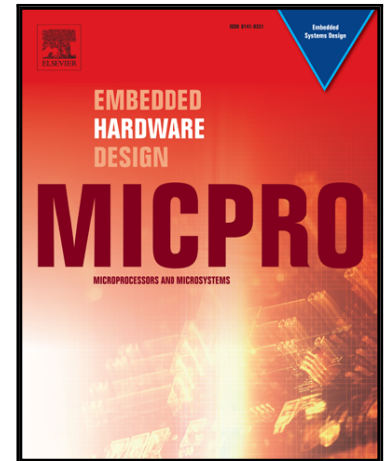


ON-Chip Peripherals are ON for Chaos – An Image fused Encryption

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**Highlights:**

- First work to use synthetic image with True randomness of FPGA in RGB encryption
- Lorenz, Lü and Cellular Automata chaotic systems employed in three planes
- Large possible key space of  $10^{103} \times 8^{65536}$  with low correlation metrics
- Encryption quality analyses prove the superiority of proposed RGB encryption scheme
- Offers resistance to Cropping / plain text invasions satisfying the NIST parameters

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