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On a theory for system-level cooling of close electronics enclosures by PCM-filled heat sinks—Exact solution and second law performance limits



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ABSTRACT

This paper takes a fundamental approach on the simple, however widespread, application of closed electronics enclosures that are passively cooled by internally mounted heat sinks filled with phase change material (PCM) and exposed to periodic temperature environments. The regime of interest is that of periodic steady state (PSS). A model that allows for an analytical solution of the Stefan-like problem in the finite PCM domain and of the energy balance coupling of the closed enclosure is devised in dimensionless terms by using a lumped formulation for the enclosure and for the PCM domain, the later being known as zero-phase modeling (Naaktgeboren, 2007). The model is one that allows for internally reversible heat transfers in the PCM and in the enclosure domains, while retaining the finiteness (irreversibilities) of the heat interactions between the environment, the enclosure, and the PCM-filled heat sinks, as well as the heat sources irreversibilities, so as to allow for meaningful outcomes. Results include melting and freezing interface location history—conversely, melting and freezing times—and a design condition for perpetual occurence of phase change; hence, latent heat interactions. Moreover, a figure of merit for passive temperature regulation of real (irreversible) electronics enclosures is proposed as a temperature attenuation effectiveness. The limit imposed by the second law of thermodynamics on the temperature attenuation effectiveness is analytically derived and shown to depend on only two (out of the four) dimensionless parameters of the model. The original expression for the enclosure temperature attenuation effectiveness in the reversible limit, obtained herein, is of theoretical and practical significance.

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1. Introduction

The problem of a closed enclosure with internal work-into-heat dissipations (heat sources) that exchanges heat with a periodic temperature environment finds application in system-level electronics cooling of outdoor systems [2,3] and on some types of buildings [4,5]. Electronics can be harmed by either internal temperature peaks or excessive temperature variations along the day-and-night cycle [6]—factors that also impact comfort on buildings.

The issue of passive temperature control of electronics by employing phase change materials (PCM)—as part of Latent Heat Thermal Storage (LHTS) systems—has been mainly addressed by means of local temperature management, i.e., on an electronic component level [7–10], on electronic or solar module level

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[11,6,12], and on portable *device* [13,14] treatments. Such local and mesoscale [14,15] thermal management approaches deal mostly with the mitigation of local temperature peaks.

In order to address the issue of temperature fluctuation along the day-and-night cycle of larger systems, a *system-level* temperature control may be used [3,5,16]. In periodic steady state (PSS) regime, the temperature of the enclosure (system) is expected to oscillate around a mean enclosure temperature. Therefore, reducing the enclosure thermal amplitude also means reducing its peaks of temperature, thus aiding all other local and mesoscale temperature control systems in place.

Electronics enclosure with PCM-filled heat sinks are complex systems from the point of view of transport phenomena. New technologies and specific systems are often studied both experimentally and numerically. Pertinent numerical studies include solution to the Stefan problem, recirculating flows in enclosures, phase change heat transfer, conjugate (coupled) heat transfer problems, and electronics cooling. An extensive compilation on such studies can be found on this [17] review.

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Nomenclature A lumped enclosure dimensionless temperature dimensionless time-average enclosure excess κ_m amplitude, Eq. (22) temperature in PSS, Eq. (16b) area, m², Eq. (2) Α μ complex dimensionless excess temperature, $(=\theta_e + i\theta_a^*)$ external ambient temperature amplitude, K, Eq. (1) dimensionless phase change interface location, Eq. (14) п Bi Biot number (= $\bar{h}_i \ell/k_I$) dimensionless Cartesian coordinate, Eq. (14) lumped enclosure incompressible substance specific PCM density (same for solid and liquid phases), kg/m³, \mathcal{C} ρ heat, kJ/kg K, Eq. (2) Σ dimensionless extent of a PCM phase in PSS, Eq. (17) С PCM incompressible substance specific heat. kJ/kg·K, Eq. (3) dummy dimensionless time, Eq. (17) σ ħ average convective heat transfer coefficient, τ dimensionless time, Eq. (14) kJ/kg ·K, Eq. (2) Φ lumped enclosure periodic temperature phase angle, k PCM thermal conductivity, kW/m·K, Eq. (3) rad, Eq. (21) PCM latent heat of fusion, kJ/kg, Eq. (5) L lumped enclosure internal-to-external convective ϕ PCM domain length, m, Eq. (10) heat-power ratio Eq. (16a) P Μ lumped enclosure mass, kg, Eq. (2) lumped enclosure thermal inertia-power to external χ external ambient mean temperature, K, Eq. (1) convective heat-power ratio, Eq. (16a) m a positive integer number, Eq. (31) dimensionless heat flux, Eq. (14) n Р electronics enclosure steady power dissipation, external ambient periodic temperature angular velocity, Ø kW, Eq. (2) rad/s, Eq. (1) heat flux, kW/m^2 , Eq. (14) q' T temperature, K, Eq. (1) Subscripts initial time, s, Eq. (1) t periodic temperature external ambient X sharp, planar phase change interface location in x, m, а closed lumped electronics enclosure (system) е internal PCM-filled heat sinks i Cartesian coordinate, m, Eq. (3) χ L PCM liquid phase \mathbb{Z} the integer number set, Eq. (31) m melting maximum max Greek symbols index n complex temperature temporal parameter, Eq. (21) β S PCM solid phase γ complex temperature free parameter, Eq. (21) PCM-filled heat sinks wall (x = 0) w δ dimensionless phase change interface penetration coefficient, Eq. (16b) superscripts lumped enclosure temperature amplitude attenuation η near (closer to origin) side of effectiveness, Eq. (34) far (farther from origin) side of dimensionless excess temperature, Eq. (14) scale of * complementary problem dimensionless excess temper-PCM liquid phase ature, Eq. (19)

In contrast, the present study takes a more fundamental approach on the subject, by proposing a model that (i) accounts for the most significant (global) enclosure and LHTS system design parameters in dimensionless form, (ii) provides an analytical solution, and (iii) allows for finding the theoretical *performance upper bound* for the technology of passive, system-level cooling of closed electronics enclosure using PCM, in terms of temperature amplitude attenuation, that stems from the second law of thermodynamics.

It is worth noting that fundamental studies exist on sensible and on latent heat thermal storage systems [18–20] that establish second law optima parameters for the storage system for the sake of *maximizing work production* by a coupled heat engine. The second law limit derived herein is conceptually different from these, in the sense that the goal here is to *minimize the enclosure temperature amplitude of oscillation*; or, conversely, *minimize peak enclosure temperature in the PSS regime*.

Despite the lack of conversion from heat into useful work by a heat engine, the second law is still applicable to the enclosure peak temperature minimization problem. The connection is immediately recognized when one contemplates the Clausius statement of the second law [21,22] while identifying that the closed enclosure, with internal work-into-heat dissipations, dynamically syncs heat into two surroundings: (i) the periodic temperature (external)

environment, and (ii) the PCM-filled (internal) heat sinks. Since one can define an enclosure sub-system in which "no other effect" is present, in order to sync heat to a surrounding system, the Clausius statement of the second law requires that the enclosure temperature must be higher than the given surrounding. However, in the limit of reversible heat transfer, the temperature excess that causes heat to be transfered becomes negligible, giving rise to a lowest maximum enclosure temperature limit stemming from the second law of thermodynamics.

In the discipline of Heat Transfer, this equilibrium thermodynamic limit of internally reversible sensible heat exchange in single-phase systems corresponds to the model of *lumped capacitance*, and arises for asymptotically small values of the Biot number, i.e., whenever $Bi \rightarrow 0$.

The Stefan problem in this same Biot number limit is the *zero-phase Stefan problem* [1], which retains the moving boundary nature of the Stefan problem, while only allowing for internally reversible sensible and latent heat exchanges. The zero-phase Stefan problem can be thought of as a multiphase thermodynamic system in equilibrium at constant pressure (and also constant volume, if one takes the phases to be incompressible and having the same specific volume), in which heat interactions change the phase fraction between the phases, or, equivalently, the *quality* of the multiphase system in equilibrium, without changing its temperature.

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