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^{Q1} Minimal perceptrons for memorizing complex patterns

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HIGHLIGHTS

- A new complexity measure for binary patterns is proposed.
- The measure estimates the minimal network size for memorizing binary patterns.
- The predicted minimal network size agrees with simulations of machine learning.

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ABSTRACT

Feedforward neural networks have been investigated to understand learning and memory, as well as applied to numerous practical problems in pattern classification. It is a rule of thumb that more complex tasks require larger networks. However, the design of optimal network architectures for specific tasks is still an unsolved fundamental problem. In this study, we consider three-layered neural networks for memorizing binary patterns. We developed a new complexity measure of binary patterns, and estimated the minimal network size for memorizing them as a function of their complexity. We formulated the minimal network size for regular, random, and complex patterns. In particular, the minimal size for complex patterns, which are neither ordered nor disordered, was predicted by measuring their Hamming distances from known ordered patterns. Our predictions agree with simulations based on the back-propagation algorithm.

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1. Introduction

Neural signaling in synaptic networks motivated the early study of artificial neural networks, to recapitulate the learning capability of the brain. Their utility has expanded from that inception to serving as alternatives to conventional computers for input/output processing or as exemplars of parallel distributed processing, and they have been successfully applied to pattern classification [1,2] and memory storage [3–6]. Standard implementations of neural networks map inputs \mathbf{x}^{μ} to outputs z^{μ} through intermediate processing layers. The *M* input/output pairs, $\xi^{\mu} = (\mathbf{x}^{\mu}, z^{\mu})$, form a *pattern*, $\mathbf{\xi} = \{\xi^1, \xi^2, \dots, \xi^M\}$. Note that the pattern in this work refers a set of input/output pairs, not just inputs as usually defined in "pattern" classification problems. This input/output mapping can be achieved in two different ways: The neural network can either (i) learn the underlying rule for the mapping from some training pairs, or (ii) memorize the whole pattern of

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Fig. 1. Three-layer feedforward neural network.

input/output pairs and retrieve the stored outputs in response to given inputs. In either way, it is a major impediment that
the required complexity of network architectures for learning or memorizing certain patterns is, in general, unknown. In
this paper, we focus on memorizing patterns.

Designing the optimal network architecture has been known as an NP (Non-deterministic Polynomial-time) problem that requires computationally expensive search techniques and optimization [7–10]. Indeed, most attempts use empirical approaches and proceed by scanning over different network configurations while utilizing incremental [11] and/or pruning algorithms [12,13]. Simple networks may lead to insufficient memory and poor generalization, while complex networks lead to poor predictive performance by overestimating each element in patterns [6]. The required complexity of networks generally depends on the complexity of patterns for memorizing. Therefore, if the complexity of patterns and networks could be quantified, the optimal network architecture could be systematically designed.

Two popular complexity measures for patterns are Shannon entropy, the degree of uncertainty for describing a pattern [14–16], and Kolmogorov complexity, the length of the shortest computer program for generating a pattern [17]. However, the uncertainty or probability of each element in a pattern is unknown and the algorithmic complexity is itself difficult to compute. These difficulties suggest the need for a metric to quantify the practical complexity of patterns relevant for perceptrons. Here we propose a simple complexity index and relate it to the minimal network size for memorizing patterns.

This paper is organized as follows: We introduce the mathematical description of our feedforward neural network in Section 2, and a storage problem of binary patterns of different complexities in Section 3. Next, we estimate minimal network sizes for storing regular binary patterns in Section 4.1 and random binary patterns in Section 4.2, and compare them to simulation results. Then, we generalize the complexity formulation to estimate minimal network size for storing complex binary patterns in Section 5. Finally, we summarize the paper in Section 6.

22 2. Neural network

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²³ We study a three-layer feedforward neural network as shown in Fig. 1. This simple network architecture is successful ²⁴ in solving pattern recognition problems [18,19]. In addition, the universal approximation theorem proves that the three-²⁵ layer network suffices to approximate any continuous function, $z^{\mu} = f(\mathbf{x}^{\mu})$ [20]. For simplicity, we consider *N*-dimensional ²⁶ vectors of binary inputs $\mathbf{x}^{\mu} = (x_1^{\mu}, x_2^{\mu}, \dots, x_N^{\mu})$ and scalar binary outputs z^{μ} . One pattern $\boldsymbol{\xi}$ represents 2^N pairs of $(\mathbf{x}^{\mu}, z^{\mu})$, ²⁷ because each component in the *N*-dimensional input vector takes values $x_i^{\mu} = 0$ or 1. The input/output mapping requires *N* ²⁸ input nodes and a single output node. In the feedforward three-layer network, an input \mathbf{x}^{μ} is transformed into the activities ²⁹ $\mathbf{y}^{\mu} = \{y_1^{\mu}, y_2^{\mu}, \dots, y_H^{\mu}\}$ of *H* hidden nodes:

$$y_{j}^{\mu} = \sigma \Big(\sum_{i=1}^{N} w_{ji} x_{i}^{\mu} - w_{j0} \Big), \tag{1}$$

where w_{ji} is the connection weight from the *i*th input node to the *j*th hidden node, and w_{j0} is the bias of the *j*th hidden node. With these definitions, the *j*th hidden node is activated when the integrated input signal $\sum_i w_{ji} x_i^{\mu}$ exceeds the bias w_{j0} through the sigmoidal activation function, $\sigma(a) = 1/(1 + e^{-a})$. The transformation from the hidden layer to the output layer follows the same rule:

$$z^{\mu} = \sigma \Big(\sum_{j=1}^{H} v_j y_j^{\mu} - v_0 \Big), \tag{2}$$

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