Accepted Manuscript

Full Length Article

Activation of amino-based monolayers for electroless metallization of high-aspect-ratio through-silicon vias by using a simple ultrasonic-assisted plating solution

Sung-Te Chen, Yu-Syun Cheng, Yiu-Hsiang Chang, Tzu-Ming Yang, Jyun-Ting Lee, Giin-Shan Chen





Please cite this article as: S-T. Chen, Y-S. Cheng, Y-H. Chang, T-M. Yang, J-T. Lee, G-S. Chen, Activation of amino-based monolayers for electroless metallization of high-aspect-ratio through-silicon vias by using a simple ultrasonic-assisted plating solution, *Applied Surface Science* (2018), doi: https://doi.org/10.1016/j.apsusc. 2017.12.245

This is a PDF file of an unedited manuscript that has been accepted for publication. As a service to our customers we are providing this early version of the manuscript. The manuscript will undergo copyediting, typesetting, and review of the resulting proof before it is published in its final form. Please note that during the production process errors may be discovered which could affect the content, and all legal disclaimers that apply to the journal pertain.

ACCEPTED MANUSCRIPT

Activation of amino-based monolayers for electroless metallization of high-aspect-ratio throughsilicon vias by using a simple ultrasonic-assisted plating solution

Sung-Te Chen^a, Yu-Syun Cheng^b, Yiu-Hsiang Chang^{c,d}, Tzu-Ming Yang^b, Jyun-Ting Lee^{b,1}, Giin-Shan Chen^{b,*}

^a Department of Electronic Engineering, Hsiuping University of Science and Technology, Dali, Taichung 412, Taiwan

^b Department of Materials Science and Engineering, Feng Chia University, Seatwen, Taichung 407, Taiwan

^c Department of Chemical Engineering, National Tsing Hua University, Hsinchu City 300, Taiwan.

^d Mechanical and Mechatronics Systems Research Labs., Industrial Technology Research Institute, Chutung, Hsinchu County 310, Taiwan. N

^{*}Corresponding author.

E-mail: gschen@fcu.edu.tw (G.S. Chen).

¹ Current address: Department of Materials Science and Engineering, National Tsing Hua University, Hsinchu City 300, Taiwan.

Abstract

In this paper, we present the method and results of electroless plating of through-silicon via (TSV) contacts using Ni nanoparticle seeds on self-assembled monolayers (SAMs). This approach where the nanoparticles are evenly distributed and stabilized on the SAM allows the successive electroless metallization schemes such as Co-alloy barrier and Cu plug used typically in TSV as interconnects. The seeding was tested on SiO₂ layers with surfaces functionalized by an amino-based aminopropyltrimethoxysilane (APTMS) SAM. APTMS-SAM after a suitable SC-1 treatment yielded a remarkably good barrier layer, with high adhesion strength (70 MPa) and low electrical resistivity (28 $\mu\Omega$ -cm). Moreover, the SAM assisted seeding protocol was followed by an ultrasonic-assisted (or mechanically agitated) electroless-plating stage together with a relatively simple plating solution. Download English Version:

https://daneshyari.com/en/article/7835187

Download Persian Version:

https://daneshyari.com/article/7835187

Daneshyari.com