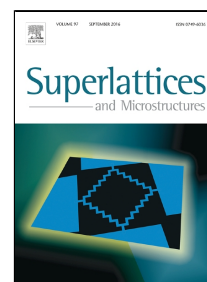


# Accepted Manuscript

Analog/RF Performance of Source-side Only Dual-k Sidewall Spacer Trigate Junctionless Transistor with Parametric Variations

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PII: S0749-6036(16)30615-2

DOI: [10.1016/j.spmi.2016.10.037](https://doi.org/10.1016/j.spmi.2016.10.037)

Reference: YSPMI 4582

To appear in: *Superlattices and Microstructures*

Received Date: 04 August 2016

Revised Date: 13 October 2016

Accepted Date: 13 October 2016

Please cite this article as: Gaurav Saini, Sudhanshu Choudhary, Analog/RF Performance of Source-side Only Dual-k Sidewall Spacer Trigate Junctionless Transistor with Parametric Variations, *Superlattices and Microstructures* (2016), doi: 10.1016/j.spmi.2016.10.037

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## HIGHLIGHTS

1. Parametric variation study on proposed JLT device (Dual-kS JLT) shows that analog/RF figure of merits of Dual-kS JLT is least sensitive to the parametric variations such as fin-width, oxide thickness, and doping concentration. This study reveals that the proposed structure is more robust to the parametric variation compared with its conventional counterpart.
2. Dual-kS JLT device design guidelines based on aspect ratio of the device is also given in this paper for improved analog/RF performance metrics.

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