Accepted Manuscript

Parallel accelerated cyclic reduction preconditioner for three-dimensional elliptic PDEs with variable coefficients

Gustavo Chávez, George Turkiyyah, Stefano Zampini, David Keyes



PII:	S0377-0427(17)30595-2
DOI:	https://doi.org/10.1016/j.cam.2017.11.035
Reference:	CAM 11407
To appear in:	Journal of Computational and Applied Mathematics
Received date :	15 May 2017
Revised date :	3 November 2017

Please cite this article as: G. Chávez, G. Turkiyyah, S. Zampini, D. Keyes, Parallel accelerated cyclic reduction preconditioner for three-dimensional elliptic PDEs with variable coefficients, *Journal of Computational and Applied Mathematics* (2017), https://doi.org/10.1016/j.cam.2017.11.035

This is a PDF file of an unedited manuscript that has been accepted for publication. As a service to our customers we are providing this early version of the manuscript. The manuscript will undergo copyediting, typesetting, and review of the resulting proof before it is published in its final form. Please note that during the production process errors may be discovered which could affect the content, and all legal disclaimers that apply to the journal pertain.

Parallel accelerated cyclic reduction preconditioner for three-dimensional elliptic PDEs with variable coefficients

Gustavo Chávez^{a,*}, George Turkiyyah^b, Stefano Zampini^a, David Keyes^a

^aKing Abdullah University of Science and Technology, Thuwal, Saudi Arabia ^bAmerican University of Beirut, Beirut, Lebanon

Abstract

We present a robust and scalable preconditioner for the solution of large-scale linear systems that arise from the discretization of elliptic PDEs amenable to rank compression. The preconditioner is based on hierarchical low-rank approximations and the cyclic reduction method. The setup and application phases of the preconditioner achieve log-linear complexity in memory footprint and number of operations, and numerical experiments exhibit good weak and strong scalability at large processor counts in a distributed memory environment. Numerical experiments with linear systems that feature symmetry and nonsymmetry, definiteness and indefiniteness, constant and variable coefficients demonstrate the preconditioner applicability and robustness. Furthermore, it is possible to control the number of iterations via the accuracy threshold of the hierarchical matrix approximations and their arithmetic operations, and the tuning of the admissibility condition parameter. Together, these parameters allow for optimization of the memory requirements and performance of the preconditioner.

Keywords: Preconditioning, Cyclic reduction, Hierarchical matrices.

1 1. Introduction

This work focuses on the iterative solution of large-scale block tridiagonal linear systems of equations 2 that arise from the discretization of elliptic partial differential equations on structured grids. Specifically, we 3 demonstrate a parallel and scalable preconditioner based on an approximate factorization generated by the cyclic reduction algorithm [1]. Cyclic reduction uses a sequence of Schur complement reduction steps, with 5 each step eliminating half of the unknowns. While an exact cyclic reduction would result in prohibitively 6 expensive dense matrix blocks, we exploit the data-sparsity of these resulting blocks by approximating them in a hierarchically low-rank form featuring log-linear storage. This work builds on [2, 3], where a fast direct 8 solver was introduced based on the synergy of parallel cyclic reduction and hierarchical matrices, and named 9 accelerated cyclic reduction (ACR). 10

Iterative methods are advantageous for large-scale scientific computing since they feature tractable complexity and scalability, but their convergence is problem dependent. Direct methods, in contrast, guarantee

*Corresponding author ${\it Email\ address:\ gustavo.chavezchavez@kaust.edu.sa\ (Gustavo\ Chávez\)}$ Download English Version:

https://daneshyari.com/en/article/8901714

Download Persian Version:

https://daneshyari.com/article/8901714

Daneshyari.com