Accepted Manuscript

Analyses of Current-Voltage Characteristics using Derivative Methodology

Wei-Fu Wang, Kai-Yuan Cheng, Meng-Chyi Wu, Kuang-Chien Hsieh

PII: S0038-1101(18)30207-7

DOI: https://doi.org/10.1016/j.sse.2018.08.002

Reference: SSE 7457

To appear in: Solid-State Electronics

Received Date: 10 April 2018 Revised Date: 20 July 2018 Accepted Date: 3 August 2018



Please cite this article as: Wang, W-F., Cheng, K-Y., Wu, M-C., Hsieh, K-C., Analyses of Current-Voltage Characteristics using Derivative Methodology, *Solid-State Electronics* (2018), doi: https://doi.org/10.1016/j.sse. 2018.08.002

This is a PDF file of an unedited manuscript that has been accepted for publication. As a service to our customers we are providing this early version of the manuscript. The manuscript will undergo copyediting, typesetting, and review of the resulting proof before it is published in its final form. Please note that during the production process errors may be discovered which could affect the content, and all legal disclaimers that apply to the journal pertain.

ACCEPTED MANUSCRIPT

Analyses of Current-Voltage Characteristics using Derivative Methodology

Wei-Fu Wang*,¹, Kai-Yuan Cheng¹, Meng-Chyi Wu¹, and Kuang-Chien Hsieh^{1,2}

 1 Institute of Electronic Engineering, National Tsing Hua University, Hsinchu 30013, Taiwan

²Center for Nanotechnology, Materials Science and Microsystems, National Tsing Hua

University, Hsinchu 30013, Taiwan

*Corresponding author

E-mail: elec5108@gmail.com

Phone:+886-911-720-921

Abstract

An alternative methodology to the modified Norde's model is presented to determine

both series and shunt parasitic resistance, ideality factor and the resistance-mediated "turn-on"

voltage of diodes by measuring derivatives of I-V characteristics. Experimental results follow

to support the theoretical investigation and demonstrate a self-consistency check.

Keywords: Diode model; Norde plot; ideality factor; parasitic resistance; turn-on voltage

1. INTRODUCTION

p-n junction diodes or Schottky diodes are known for their rectifying characteristics.

Current-voltage and capacitance-voltage measurements are important tools used to

characterize diodes[1], [2]. Norde[3] in 1979 and many others[4]–[6] since then have used

modified I-V plots to determine the ideality factor and barrier height of Schottky diodes

even if associated with high series resistance. On the other hand, the forward diode current

increases sharply above the so-called "turn-on" or "knee" voltage, generally estimated by

the tangent intercept approach or simply defined at which a certain current is reached. In

this work, we expand on Norde's model and present a methodology based on derivatives of

1

Download English Version:

https://daneshyari.com/en/article/8942061

Download Persian Version:

https://daneshyari.com/article/8942061

Daneshyari.com