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A model for non-volatile electronic memory devices with strongly correlated materials

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Abstract

The behavior of a model for non-volatile electronic memory devices is discussed. The resistance switching mechanism that gives place to the memory effect is due to an effective doping driven transition in small domains at the interface. © 2004 Elsevier B.V. All rights reserved.

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To take advantage of strong correlation effects for the design of electronic devices has long been a goal of solid state physicists. Significant progress has been achieved in recent years in fabrication techniques such as pulsed laser deposition, which is opening the way for the fabrication and investigation of metal-insulator-metal (MIM) structures that display different types of resistance switching. These devices are prototypes for two-terminal non-volatile random access memories and the resistance switching is obtained through the application of a short voltage or current pulse. The insulator of these MIM structure is usually a transition metal oxide [1-5] in which the electron correlations might be expected to play an important role. The switching effects are normally correlated with observed hysteresis in the I-V characteristics. These systems are collectively called resistance random access memories (RRAM) and the large resistance variations with applied voltage, i.e., external electric field is termed colossal electroresistance effect (CER).

The goal of the present work is to investigate the predictions for the switching and hysteresis properties of a recently proposed model [6,7] that introduces strong correlation effects due to carrier injection as the driving mechanism for CER. We shall first demonstrate how the model captures the qualitative aspects of resistance switching and then correlate the switching to the predictions for the I-V characteristics and the microscopical state of the system. We shall argue that an effective doping driven transition at the level of small nanoscopic regions near the electrodes is responsible for the switching and hysteresis effects. We also discuss what are the implications of such a scenario, in particular for non-volatility and switching speeds.

The model (Fig. 1) assumes the existence of an insulating (and inert) medium with a non-percolating structure of metallic domains that might correspond to defects, grains, phase separated regions, and so on [6]. Smaller domains are closer to the electrodes and are called "top" and "bottom". A large domain occupies the bulk of the system that is called "central". Carriers tunnel between domains under the action of an external electric field (or applied voltage). The probability of charge transfer depends on phenomenological parameters such as the tunneling rates, the number of states

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Fig. 1. Schematic view of the model with top and bottom electrodes (black), insulating medium (grey), smaller top and bottom domains (green and red) and large middle domain (blue). (For interpretation of the references to colour in this figure legend, the reader is referred to the web version of this article.)

in the domains and their occupation (see Ref. [6]). The model is mathematically defined by the following system of rate equations,

a 1.

$$\frac{\mathrm{d}n_i^{\mathrm{o}}}{\mathrm{d}t} = \Gamma_i^{\mathrm{eb}} N_{\mathrm{e}} n^{\mathrm{e}} (1 - n_i^{\mathrm{b}}) f_{\mathrm{eb}}(V) - \Gamma_i^{\mathrm{bc}} n_i^{\mathrm{b}} N_{\mathrm{c}} (1 - n^{\mathrm{c}}) f_{\mathrm{bc}}(V)$$

$$\tag{1}$$

$$\frac{\mathrm{d}n^{\mathrm{c}}}{\mathrm{d}t} = \sum_{i} \left[\Gamma_{i}^{\mathrm{bc}} N_{\mathrm{b}} n_{i}^{\mathrm{b}} (1-n^{\mathrm{c}}) f_{\mathrm{bc}}(V) - \Gamma_{i}^{\mathrm{ct}} n^{\mathrm{c}} N_{\mathrm{t}} (1-n_{i}^{\mathrm{t}}) f_{\mathrm{ct}}(V) \right]$$

$$\tag{2}$$

$$\frac{\mathrm{d}n_i^{\mathrm{t}}}{\mathrm{d}t} = \Gamma_i^{\mathrm{ct}} N_{\mathrm{c}} n^{\mathrm{c}} \left(1 - n_i^{\mathrm{t}}\right) f_{\mathrm{ct}}(V) - \Gamma_i^{\mathrm{te}} n_i^{\mathrm{t}} N_{\mathrm{e}}(1 - n^{\mathrm{e}}) f_{\mathrm{te}}(V) \quad (3)$$

where $\Gamma_i^{\alpha\beta}$ denote the tunneling rates between the electrodes and domains, and α , β =e, t, c, b denote "electrode", "top", "central" and "bottom" and the subindex *i* labels the domains. N_{α} is the total number of states in electrode or domain α , and n^{α} is the occupation. $f_{\alpha\beta}(V)$ describes the dependence of the transfer probabilities between α and β on the given external voltage protocol V(t). The actual value of these functions are material dependent [8] and their determination is beyond the scope of the present work.

The domain structure assumed in this model is motivated from a rather universal aspect of strongly correlated perovskites: the spatial inhomogeneity (or phase separation) that occurs at the nanoscale. This has recently emerged as one of the most spectacular properties of TMOs and possibly responsible for much of the interesting physics that they display [9,10]. Clearly, its origin is in the variety of competing electronic states that the strong correlation in perovskite 3*d*-electron systems bring about [11]. This feature also leads to another important and ubiquitous characteristic of TMOs that of usually having a reach phase diagram, where different phases can be access through pressure, temperature, doping, etc. In fact, the possibility of a doping driven transition within the electronic state of the domains was proposed in a recent paper [7] as the mechanism responsible for resistance switching. Following that work we allow for a doping driven metal-insulator transition in the present work, so when the level of occupation of a domain attains a given threshold we shall assume that the tunelling function f(V) in Eqs. (1)–(3) is renormalized by a factor that takes into account the decrease of states at the Fermi energy when a (Mott) gap opens in the density of states of the domains. Concretely, we take for the tunelling function $f_{\rm mit}(V) = e^{-\Delta/T} f(V)$ if the occupation of the corresponding domain is within 10% of half-filling, or else $f_{mit}(V)=f(V)$. Δ is the size of the Mott gap and T is the temperature. There are two important points to make: i) it is crucial to emphasize that the phase control operates only at the level of the small domains through carrier injection. Evidently, the amount of charge that one can inject to the MIM structures is not enough to macroscopically dope them, however, the small domains are structures of typical size of tens of nanometers [12] and thus even small amounts of charge would be sufficient to effectively modify their doping (or occupation) state. ii) While we are here considering the particular case of a metal-insulator transition at the level of the domains, in general, any transition driven by doping would produce qualitatively similar results for the resistance switching properties. The key point being that as one tunes from one phase to another, the conductance properties due to the variations in the number of states at the Fermi energy, are expected to be different in each phase.

In this work we set $\Gamma_i^{eb} = \Gamma_i^{te} \equiv \Gamma^{ext} = 0.3 \times 10^{-15}$, $\Gamma^{tc} = \Gamma^{cb} \equiv \Gamma^{int} = 1.0 \times 10^{-13}$, the number of top and bottom domains to 40 and the number of states that they hold to 10^6 ; the sole central domain holds 10^9 states. The actual number of domains is not a crucial quantity of the model and has been rather arbitrarily set. The actual realistic number might hopefully be obtained by imaging techniques such as those of Ref. [13]. On the other hand, the number of states in the domains are such that produce carrier densities of similar magnitude as those experimentally reported [3].

We now turn to a discussion of the results. The basic switching behavior of the model is demonstrated in Fig. 2. The results correspond to a single Monte Carlo simulation run of 6000 time steps or units of time (uot). The system starts in an initial state in which all small domains are 20% filled and the center domain is 10%. These small occupations are consistent with systems such as lightly Cr-doped $SrTiO_3$ [2,14]. The top panel of Fig. 2 shows the voltage protocol. An external direct-current (dc) voltage bias V_{read} is continuously applied between the top and bottom electrodes and produces a carrier current. At given time intervals of 1000 uot [15], voltage pulses of short duration (10 uot) are applied. The mid panel shows the current at the top electrode, and we see that positive voltage pulses cause sharp positive current spikes while the negative pulses cause negative spikes.

The bottom panel also shows the current but in a different vertical scale that reveals more detailed behavior between the pulses in the presence of a small dc bias $V_{\text{read.}}$

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